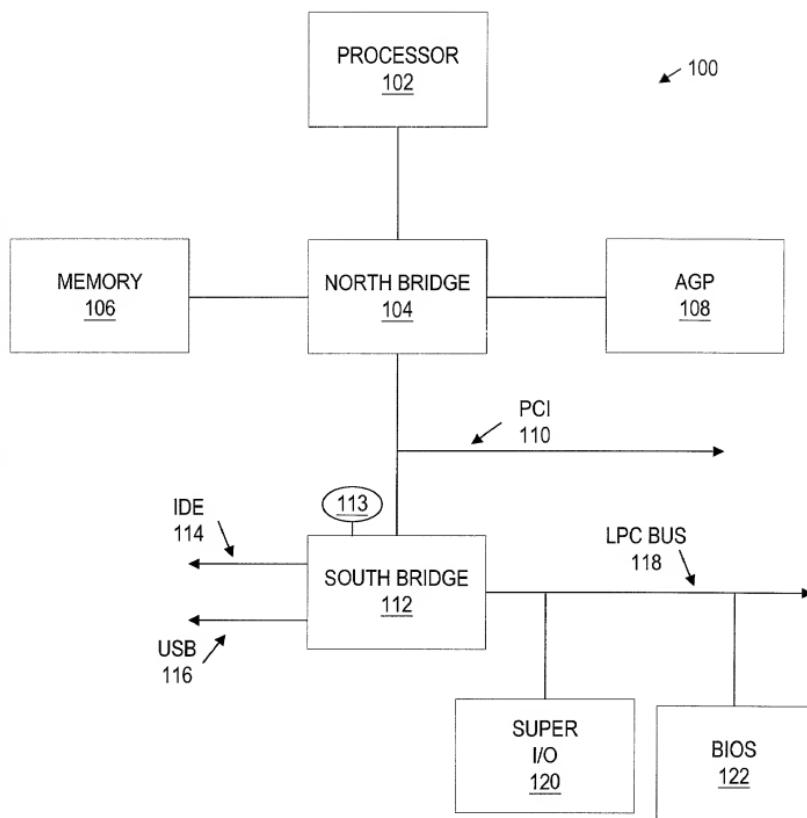
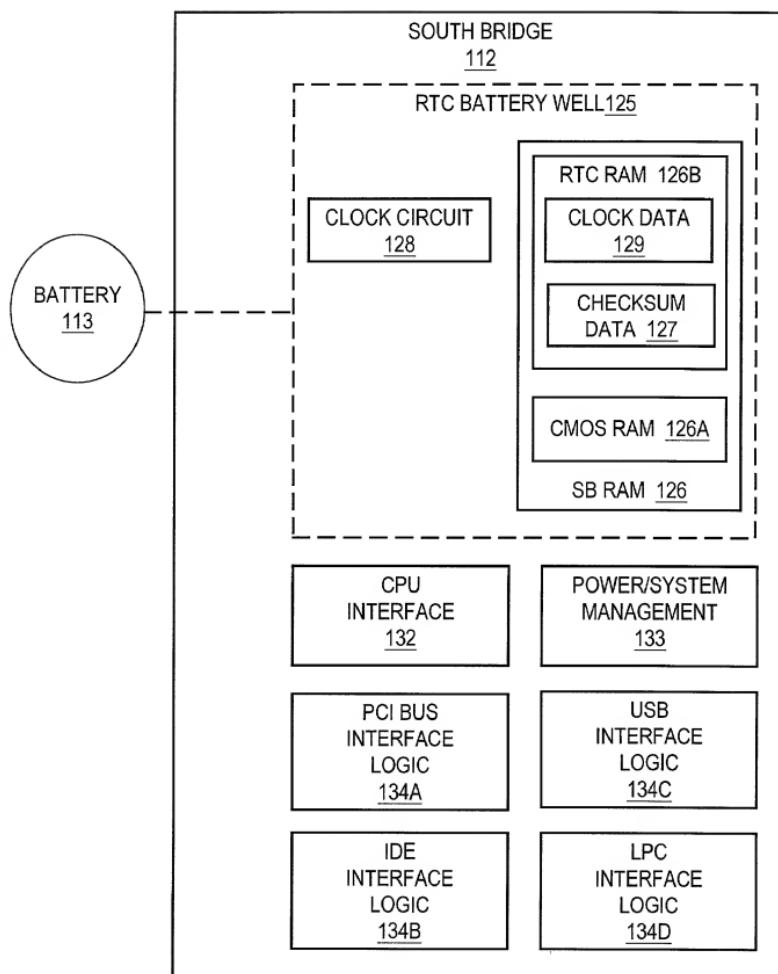


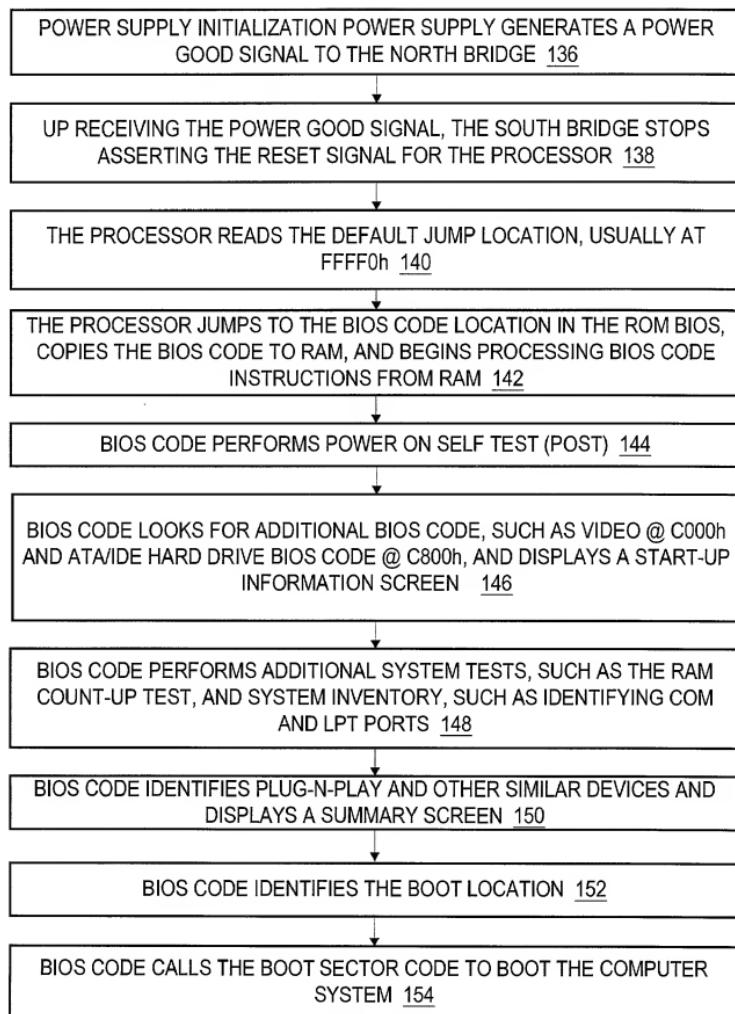
09853225 - 051101



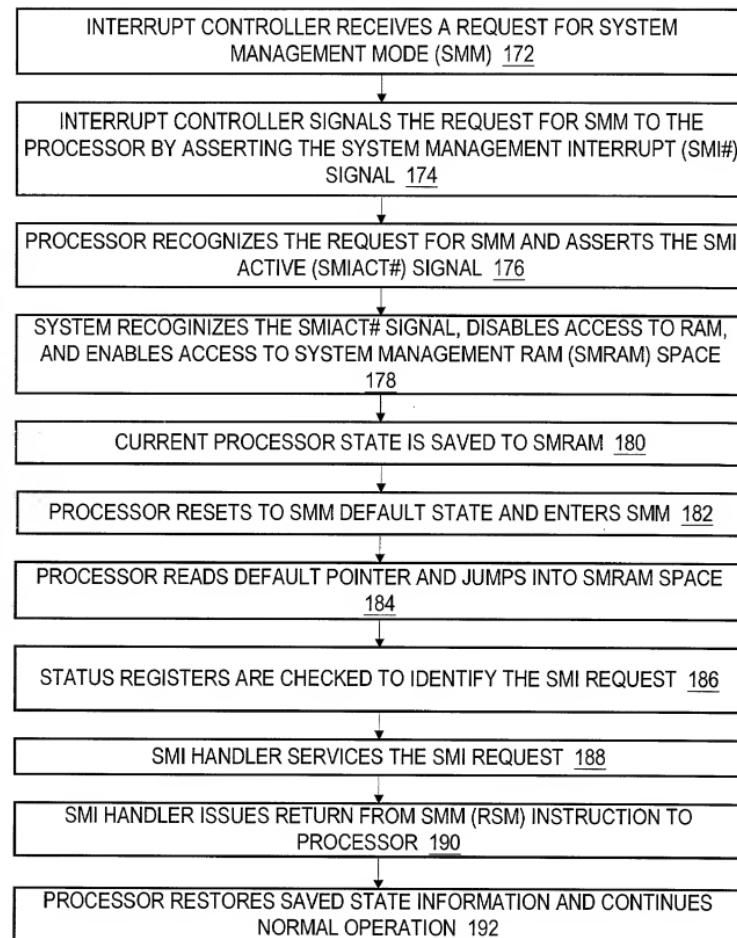
**Fig. 1A**  
**(Prior Art)**



**Fig. 1B**  
**(Prior Art)**



**Fig. 2A**  
**(Prior Art)**



**Fig. 2B  
(Prior Art)**

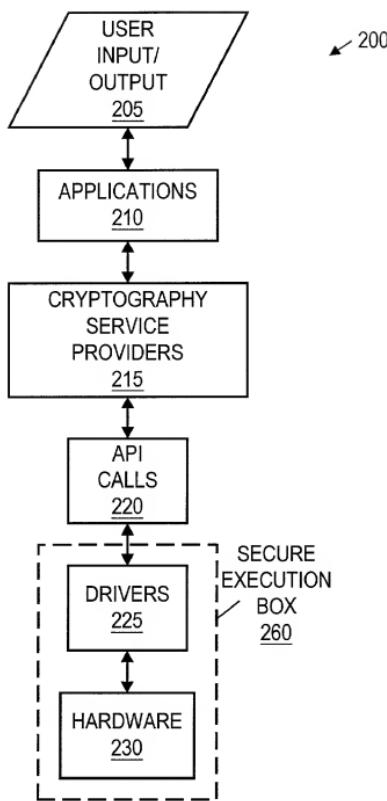
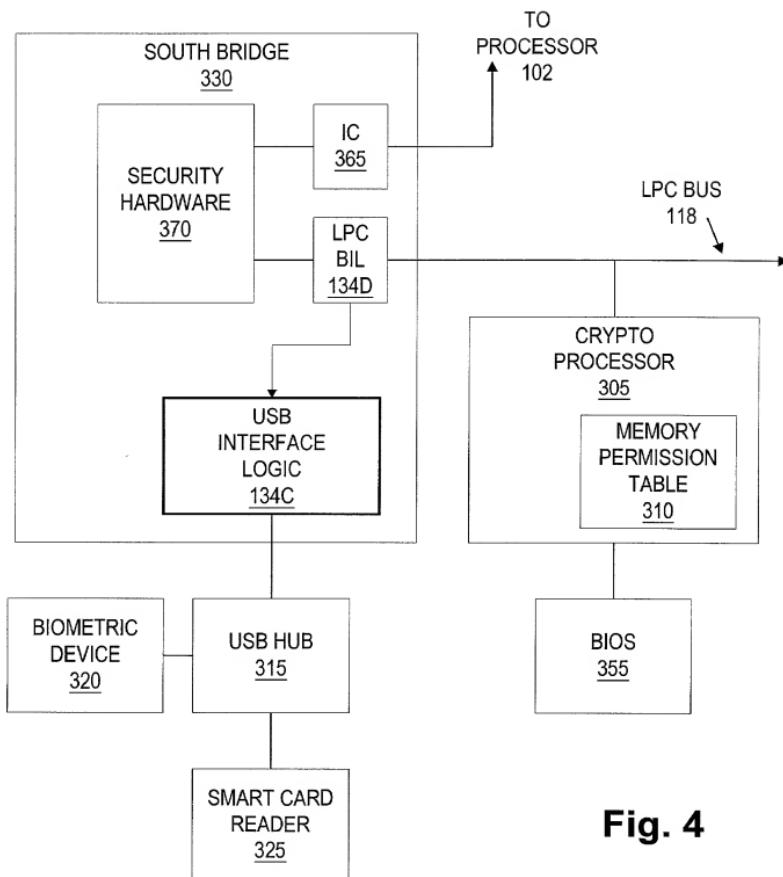
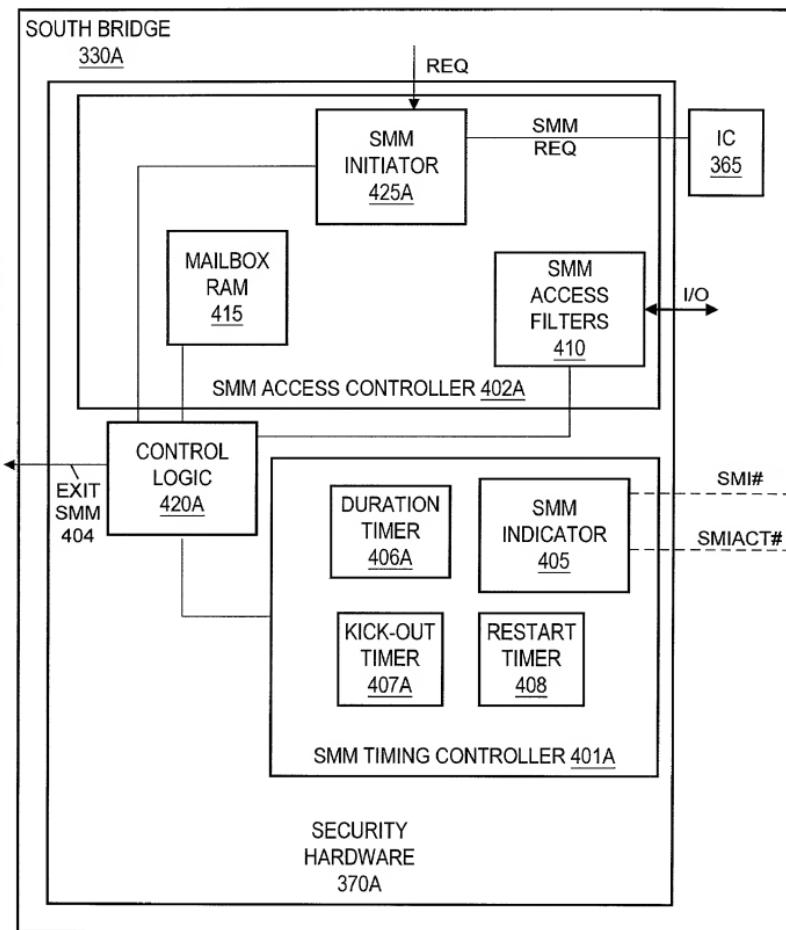


Fig. 3



**Fig. 5A**

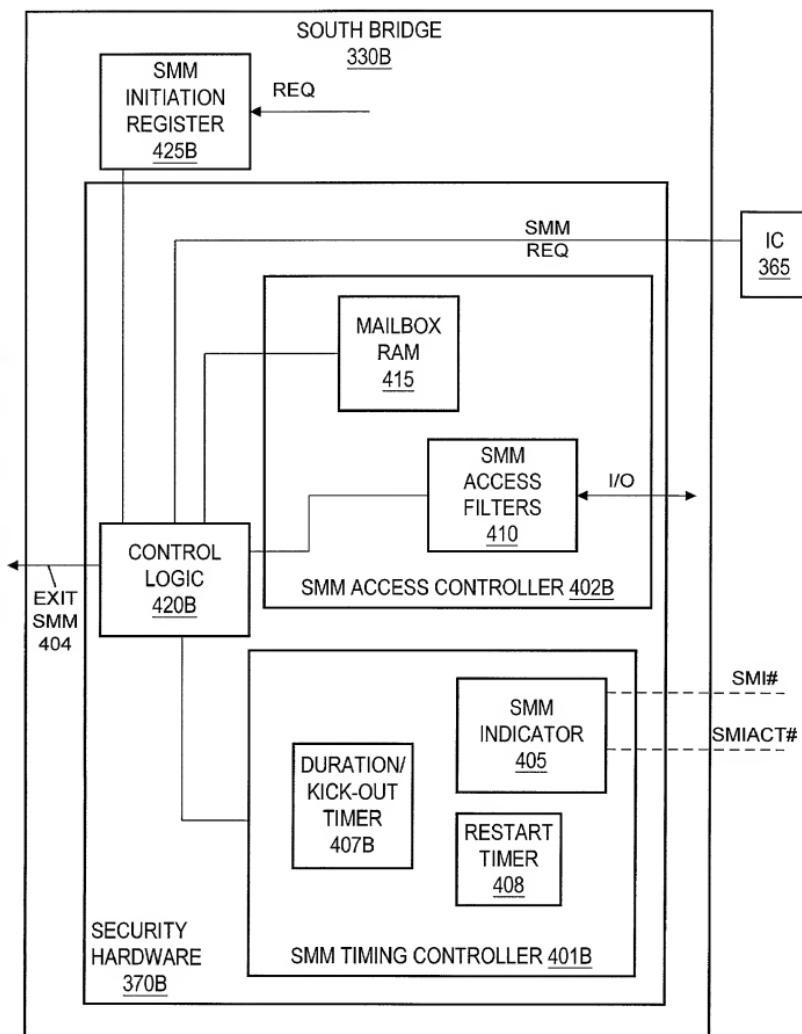


Fig. 5B

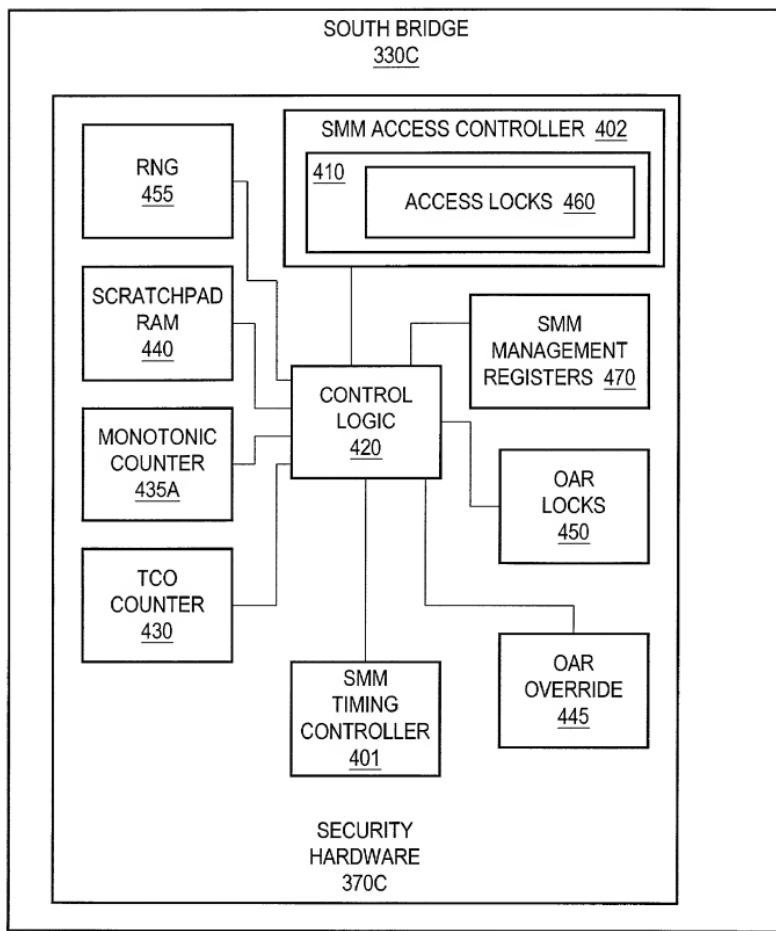


Fig. 6

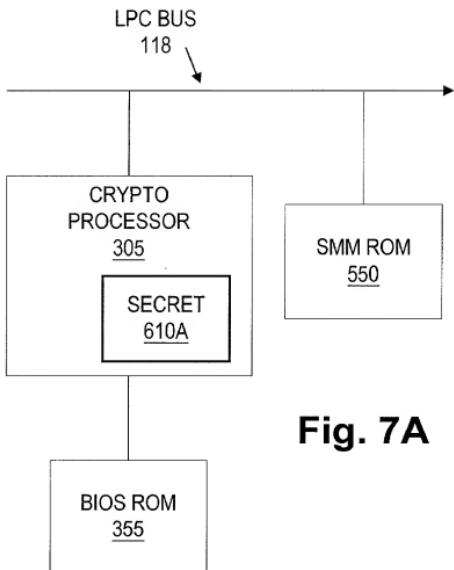


Fig. 7A

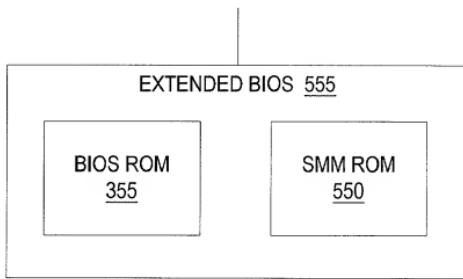
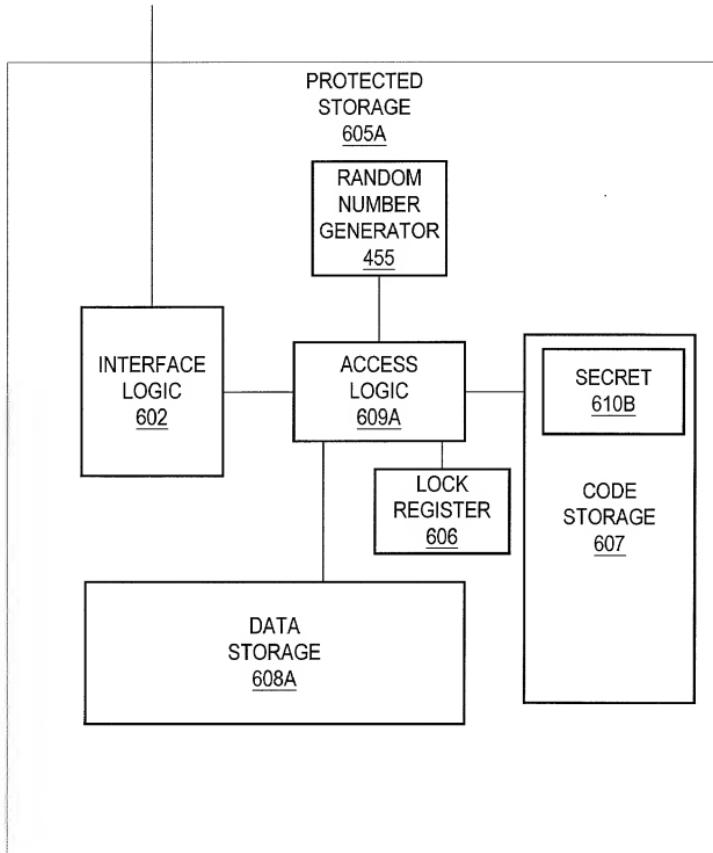


Fig. 7B

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**Fig. 7C**

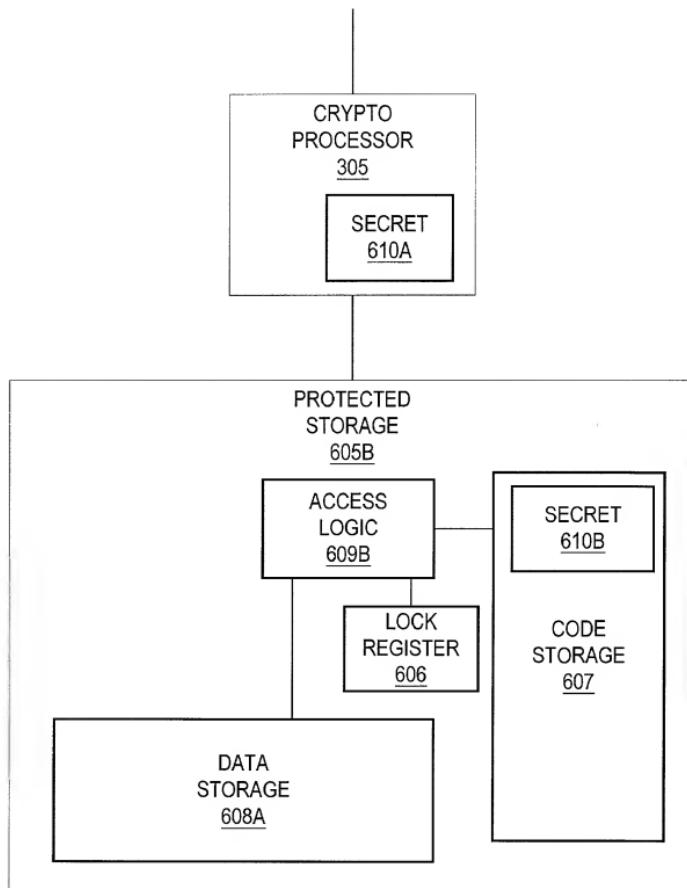
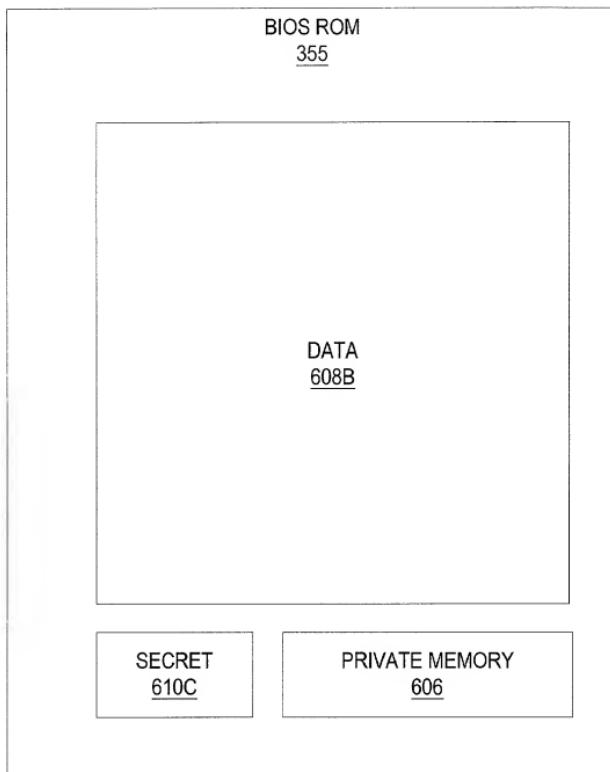
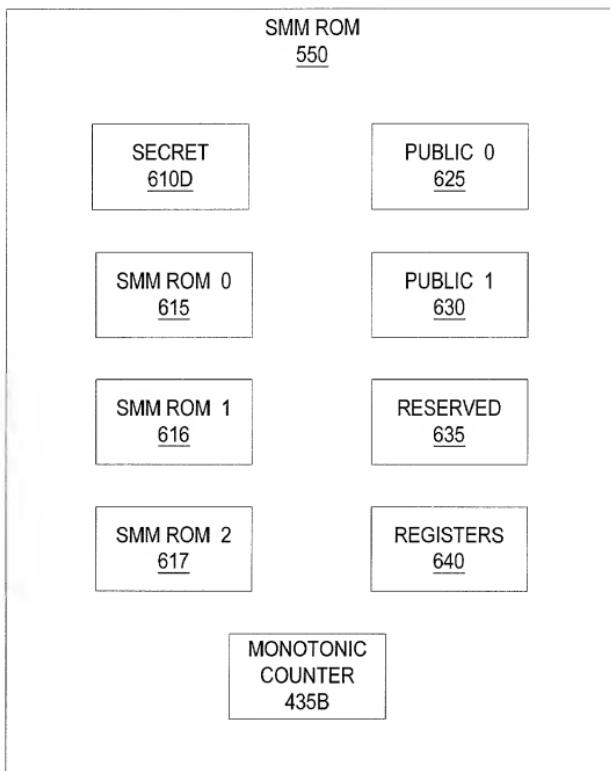


Fig. 7D



**Fig. 8A**



**Fig. 8B**

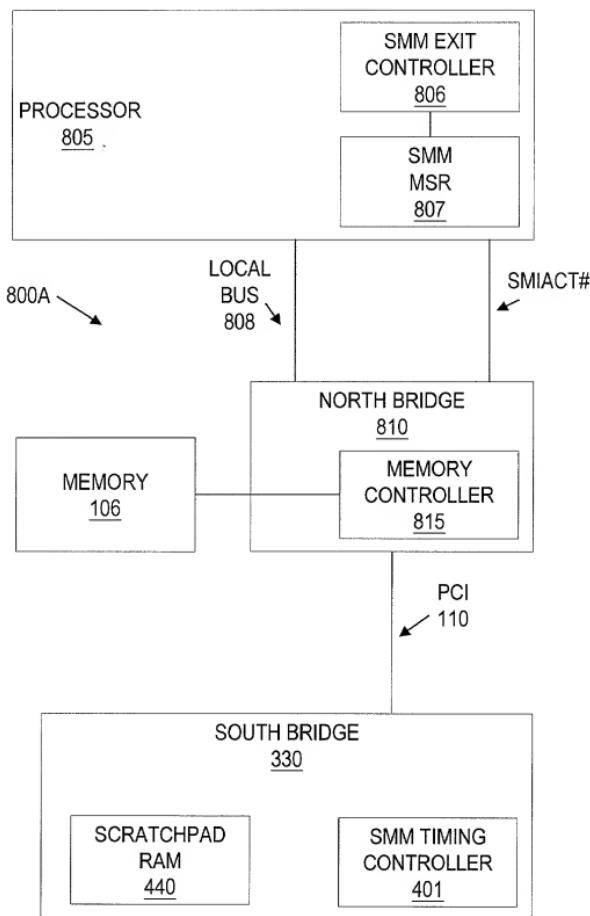


Fig. 9A

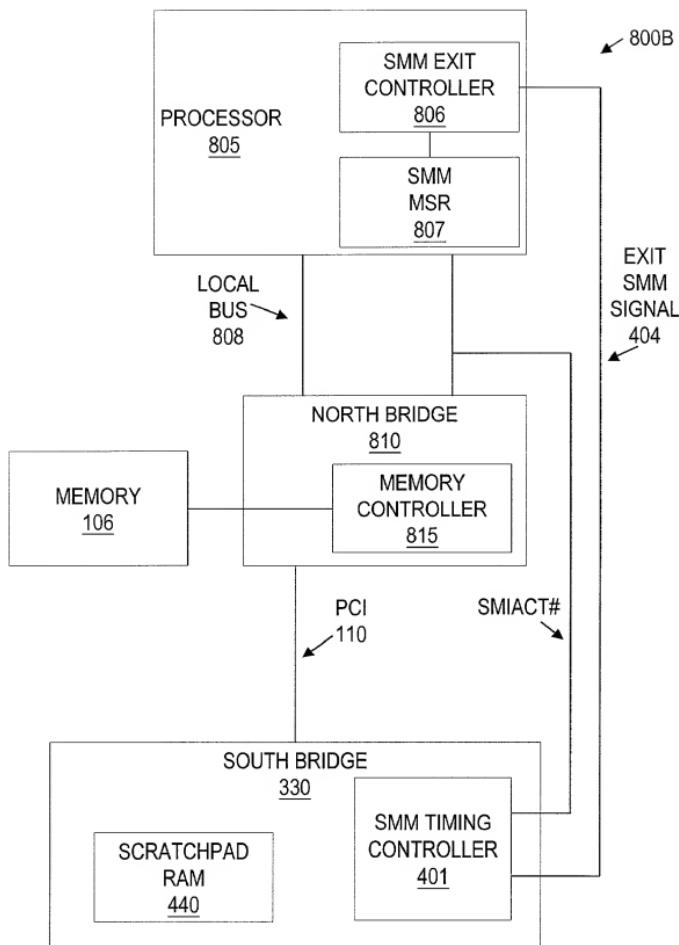


Fig. 9B

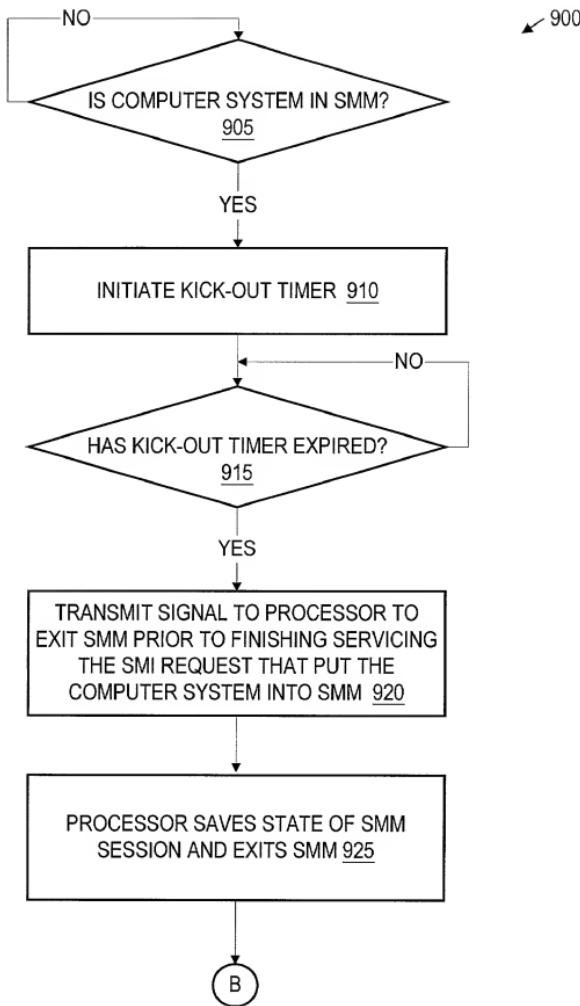


Fig. 10A

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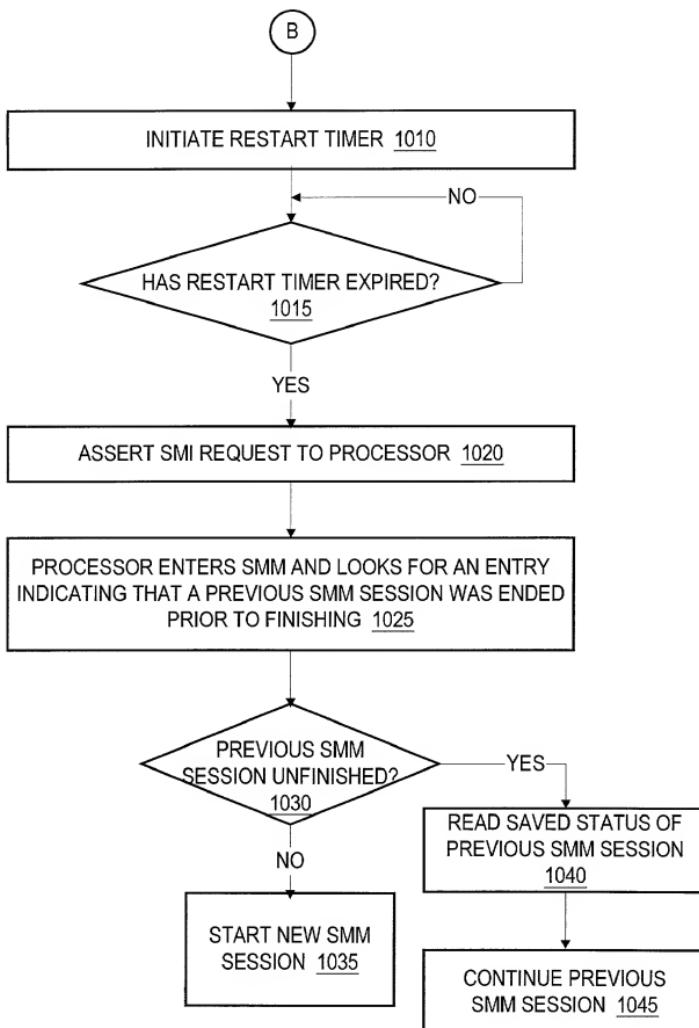
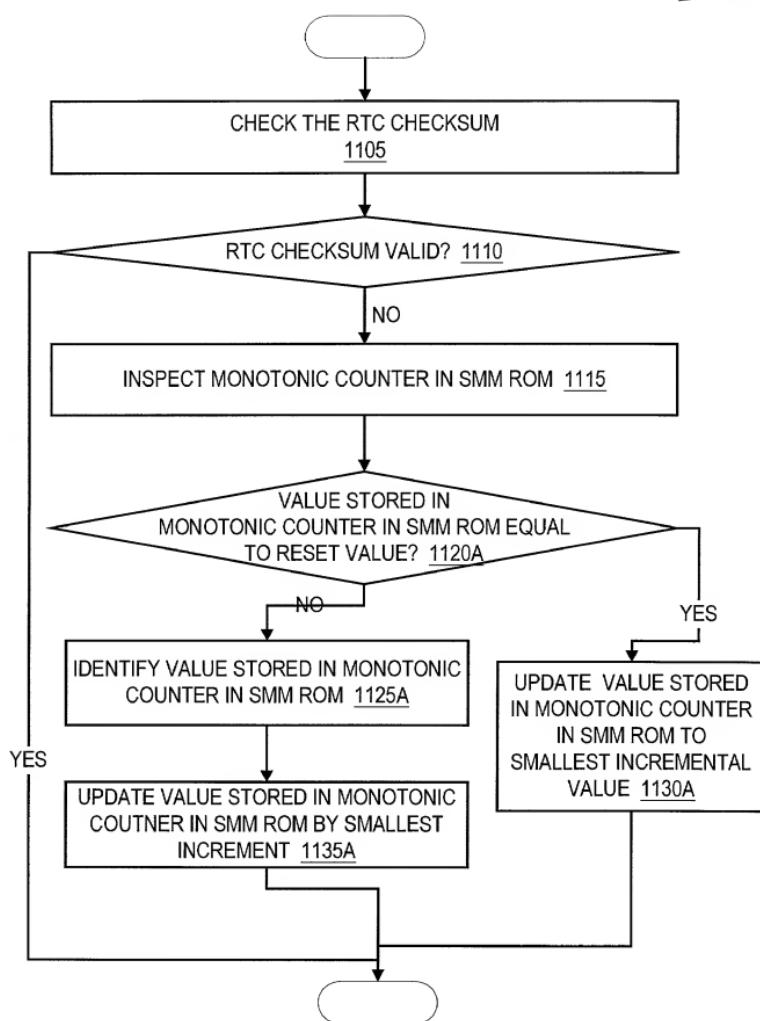


Fig. 10B

09853225-051101

**Fig. 11A**

09353225 - 051101

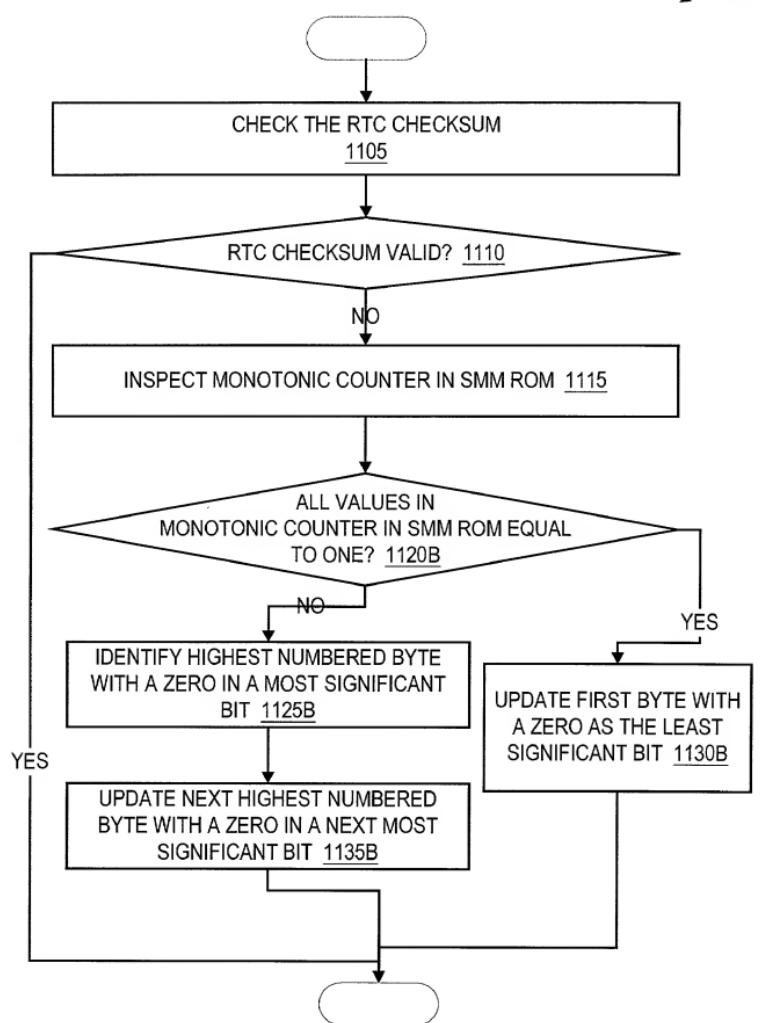


Fig. 11B

09853225 - 051401

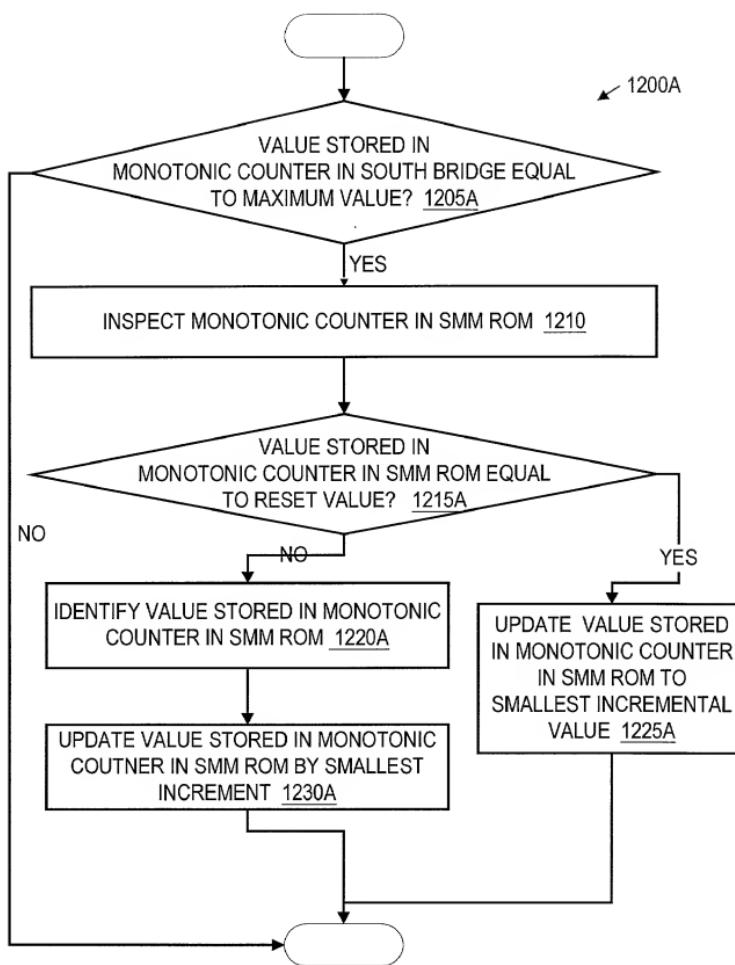
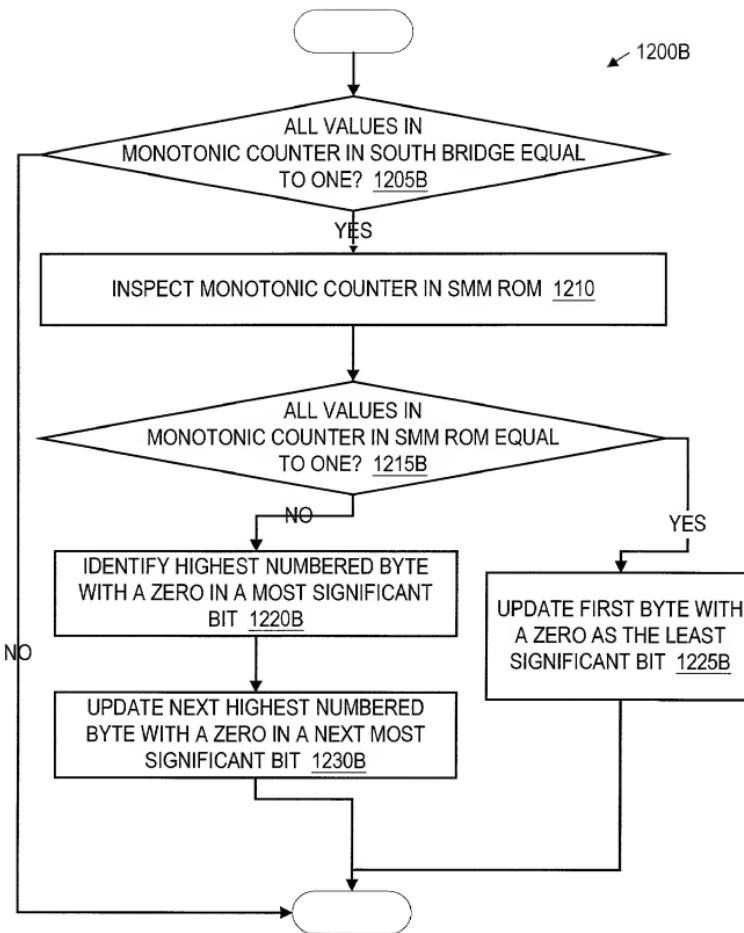


Fig. 12A

09855225, 053401

**Fig. 12B**

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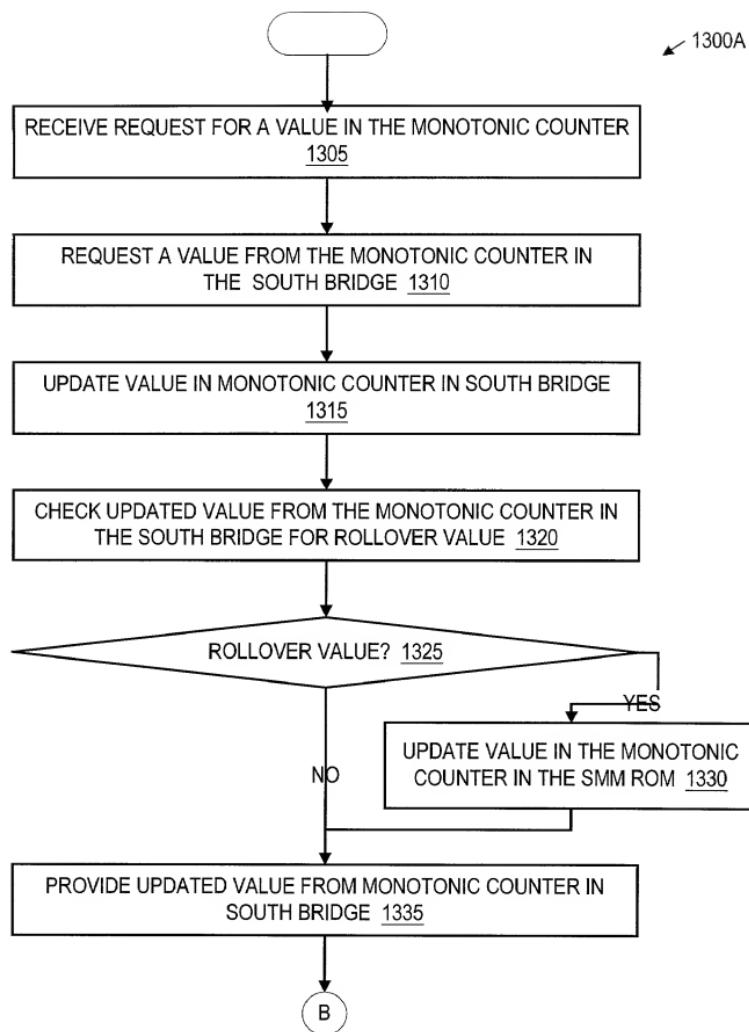


Fig. 13A

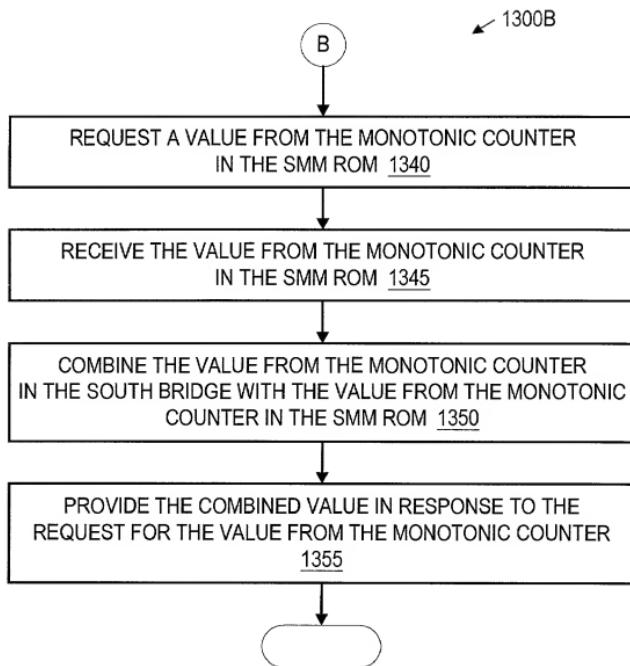


Fig. 13B

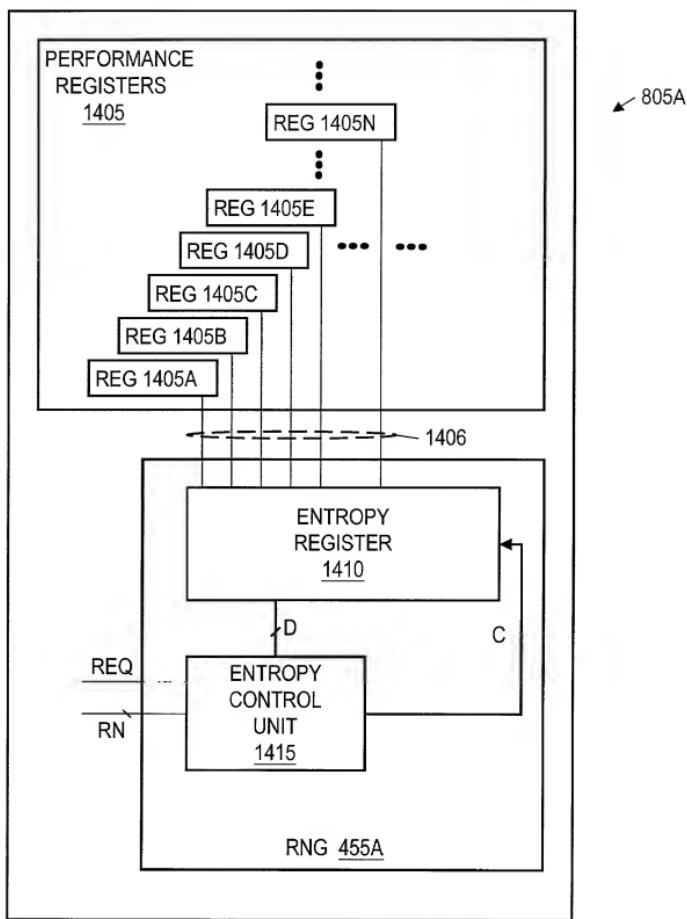


Fig. 14A

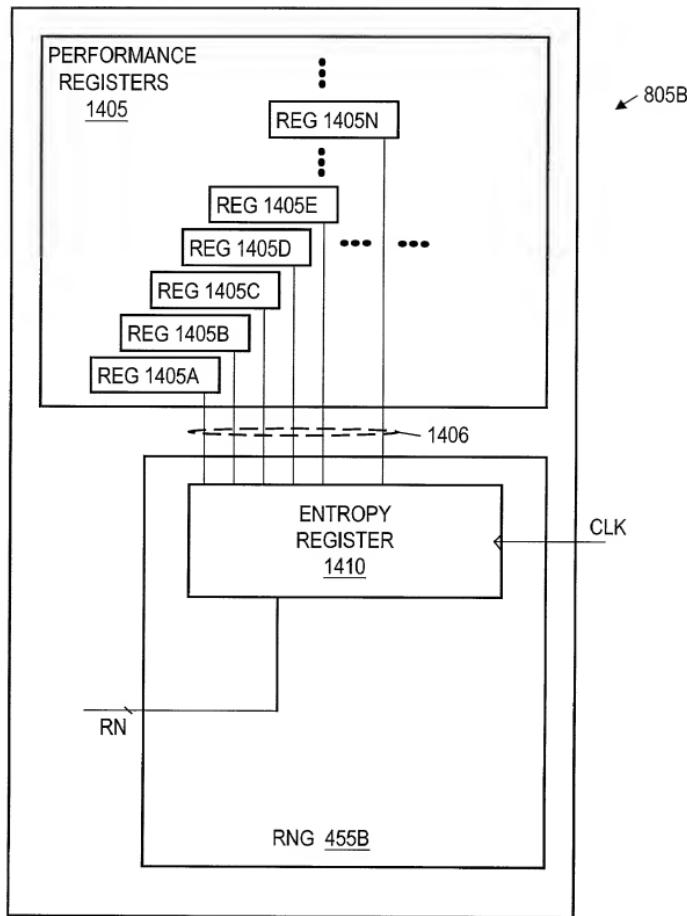


Fig. 14B

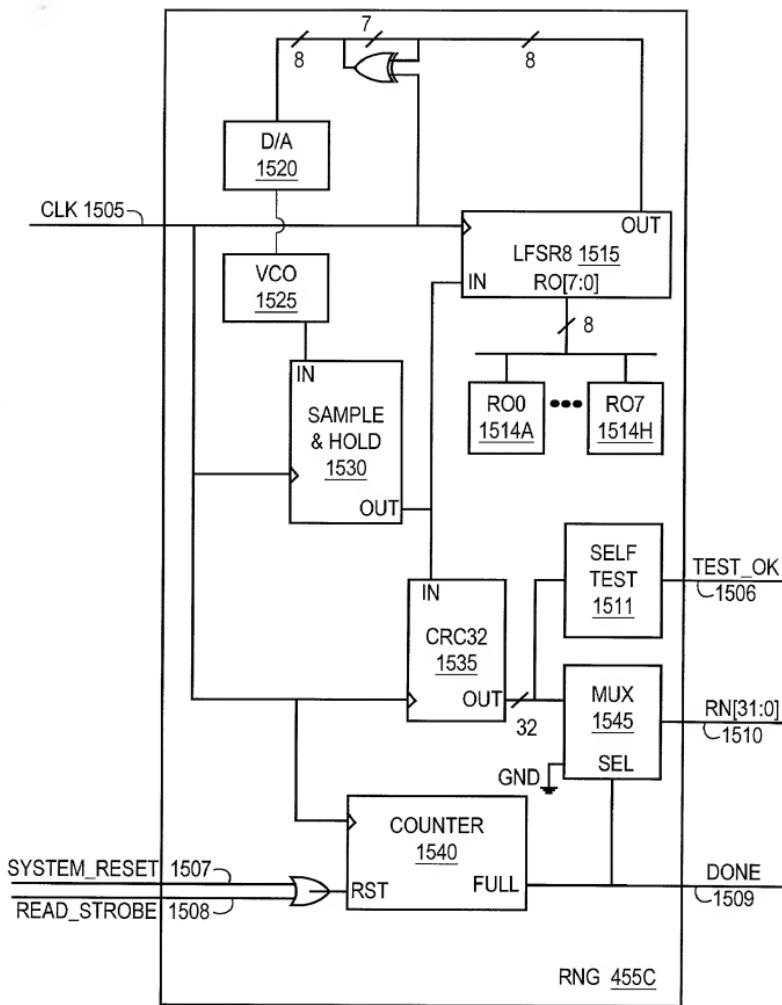


Fig. 15

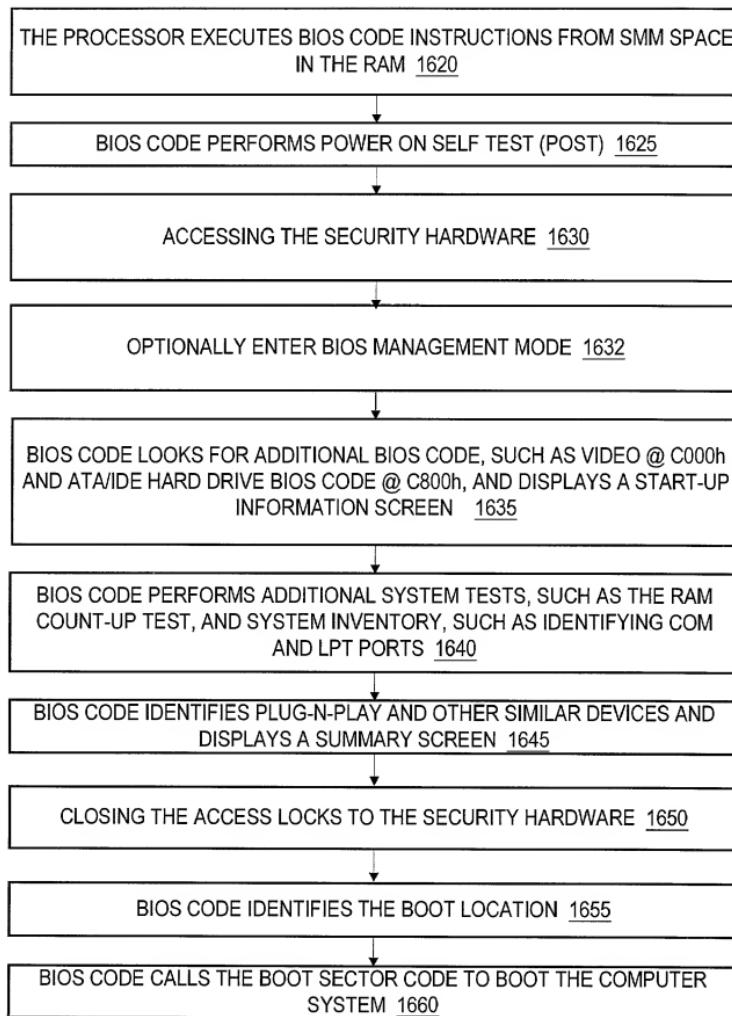
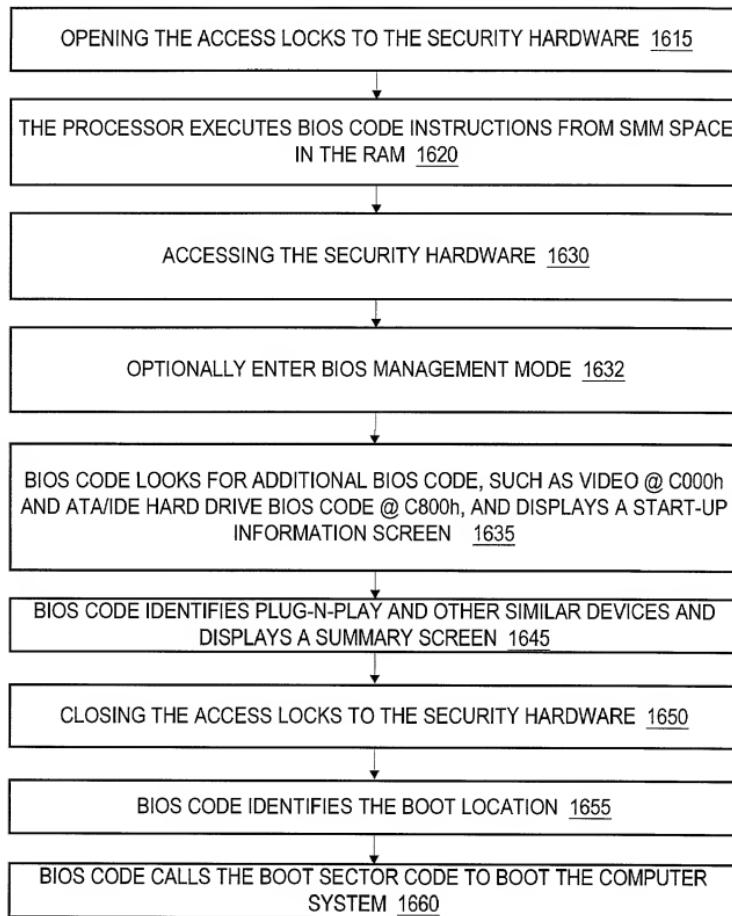


Fig. 16A



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Fig. 16B

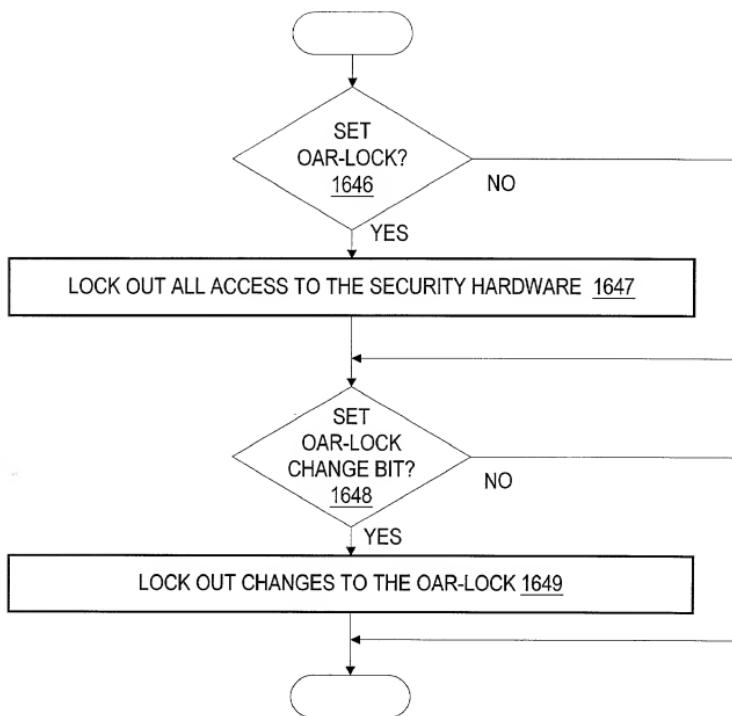


Fig. 16C

1600D

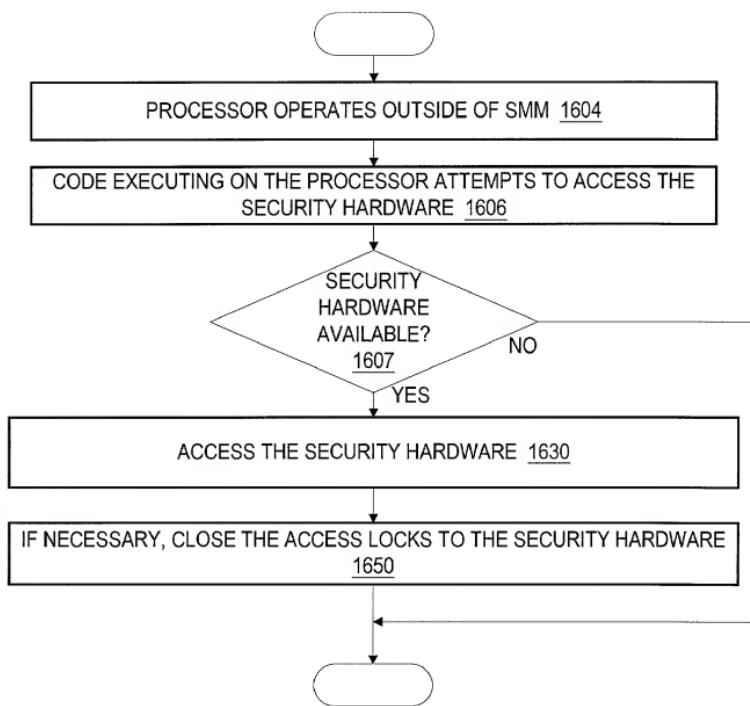


Fig. 16D

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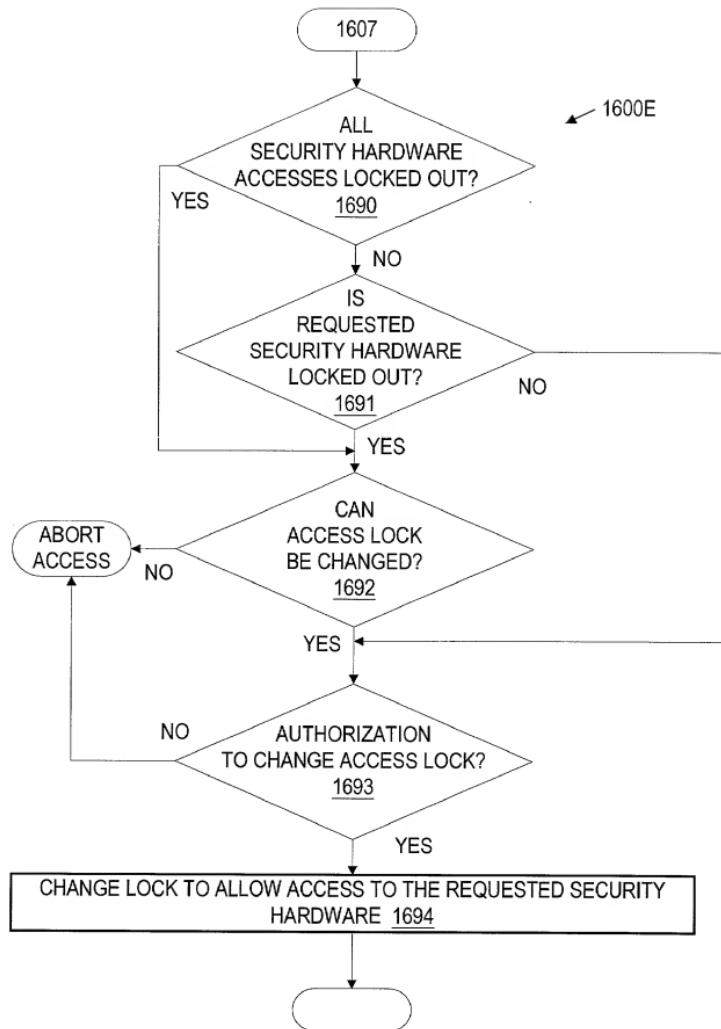
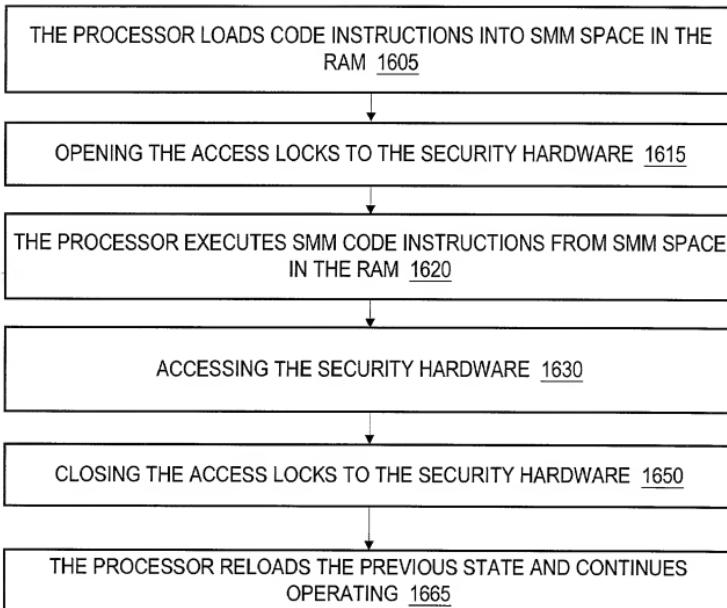


Fig. 16E

09353225 - 051401

1600F



09853225 - 051101

Fig. 16F

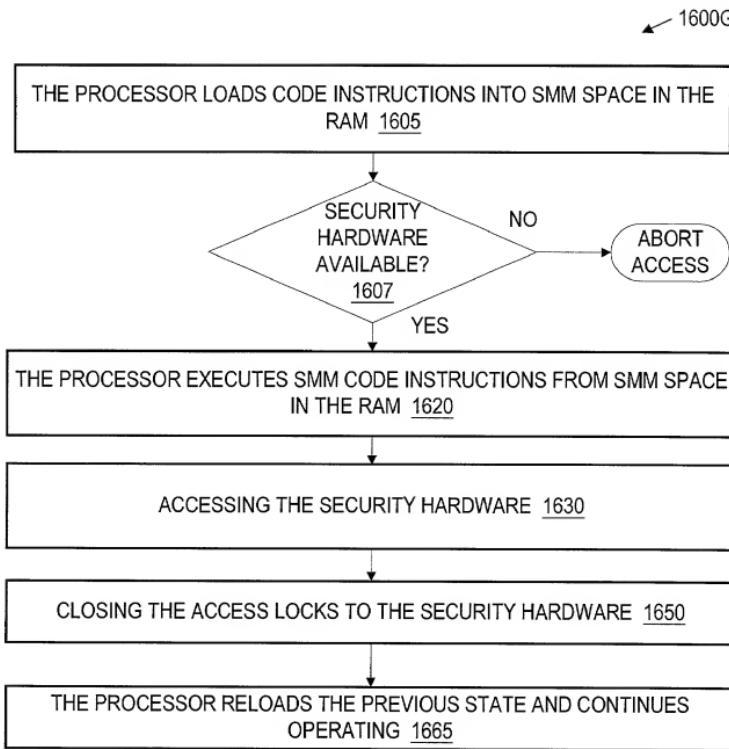


Fig. 16G

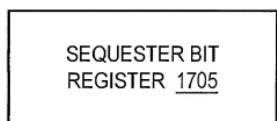


Fig. 17A



Fig. 17B

09853225 - 051101

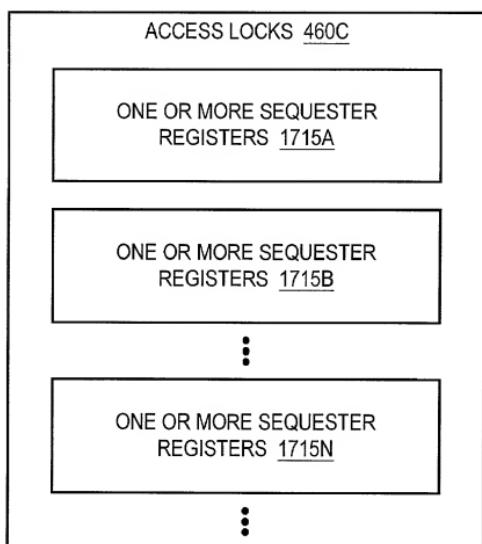


Fig. 17C

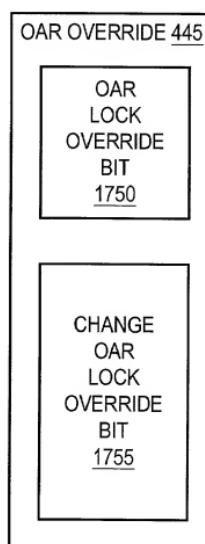
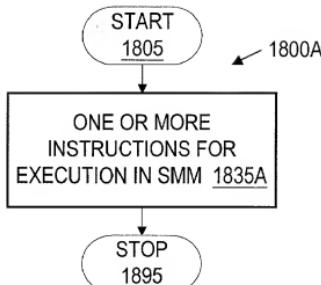
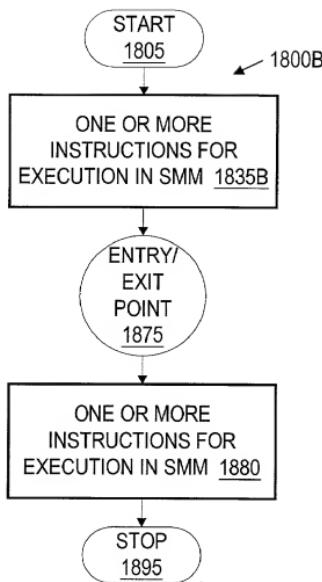


Fig. 17D

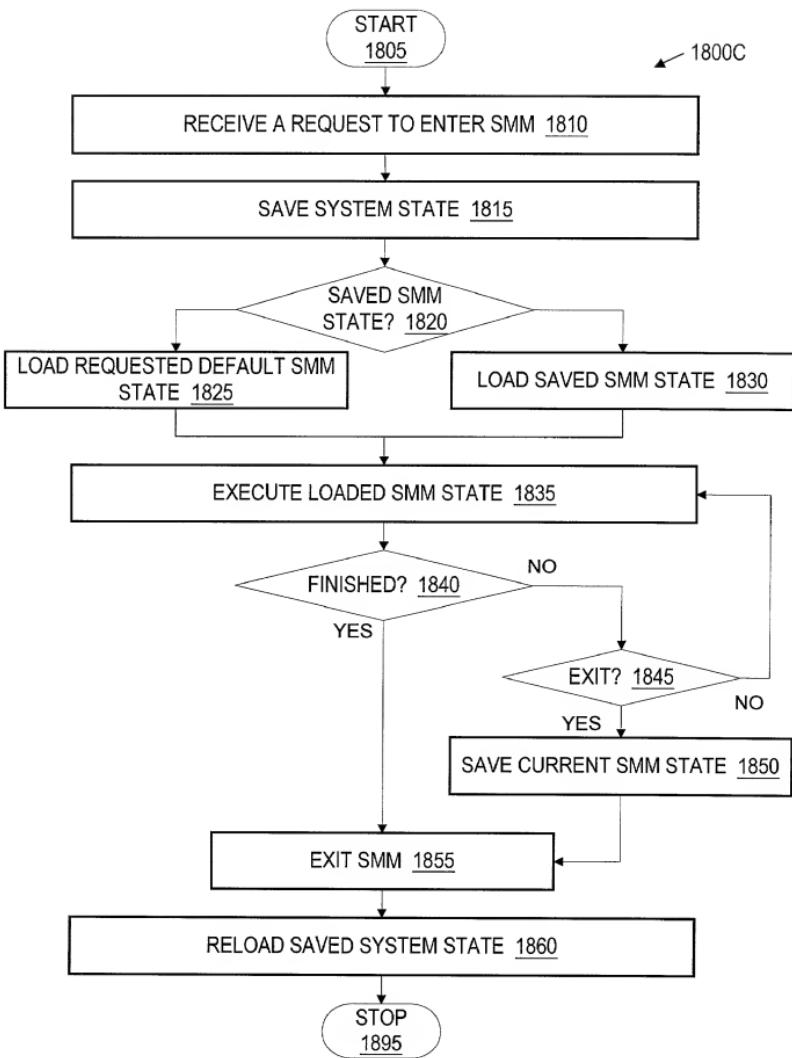


**Fig. 18A  
PRIOR ART**

09853225 - 051404



**Fig. 18B**



09853225, 051101

Fig. 18C

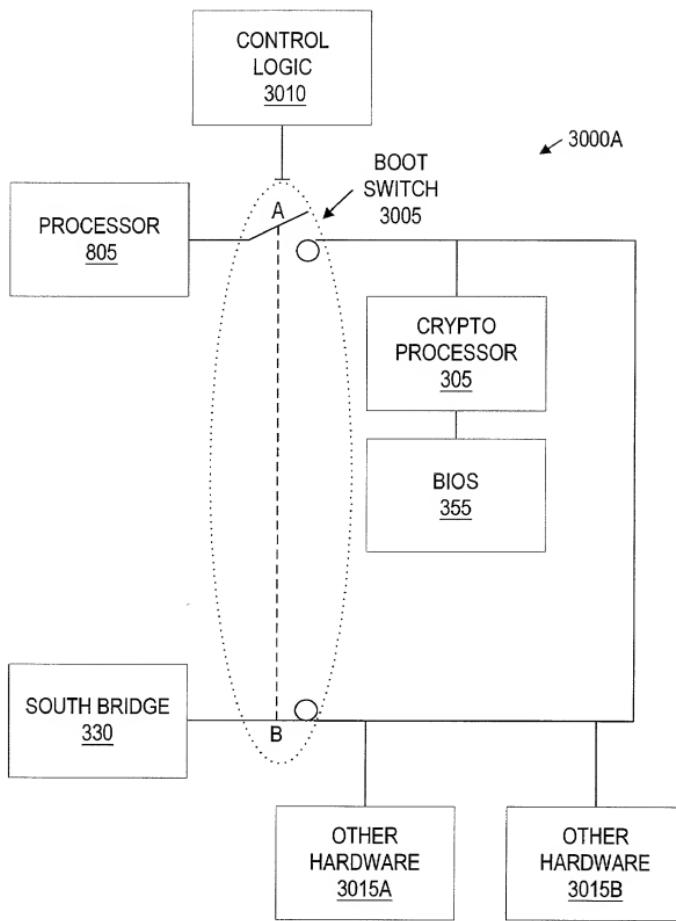


Fig. 19A

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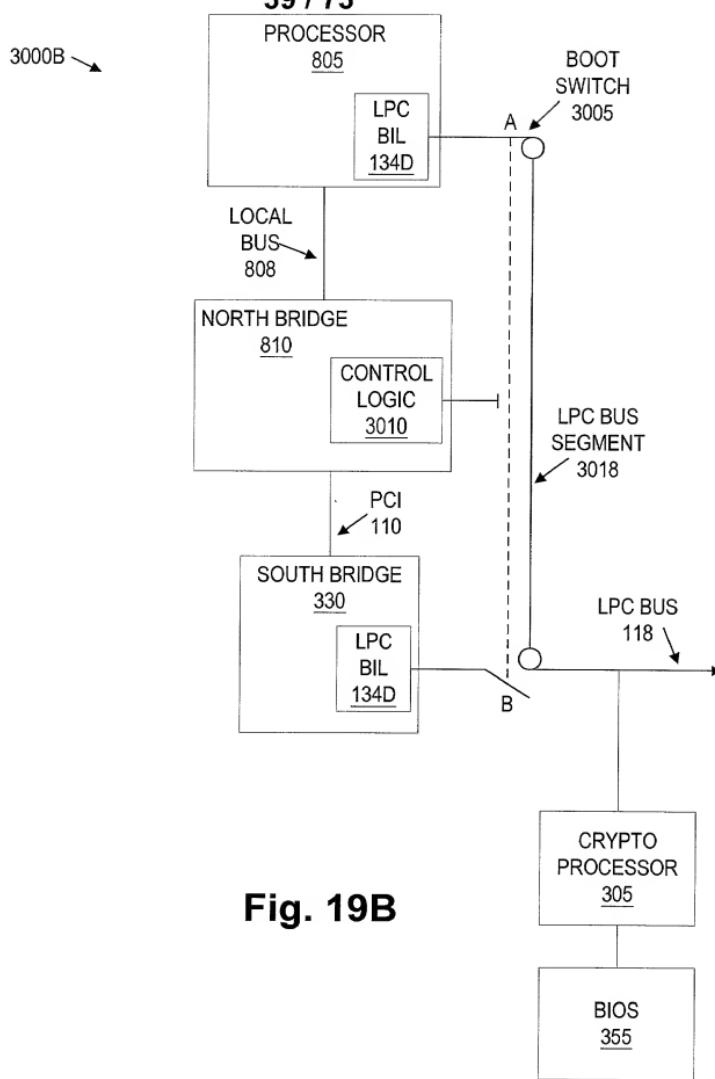


Fig. 19B

09853225.051401

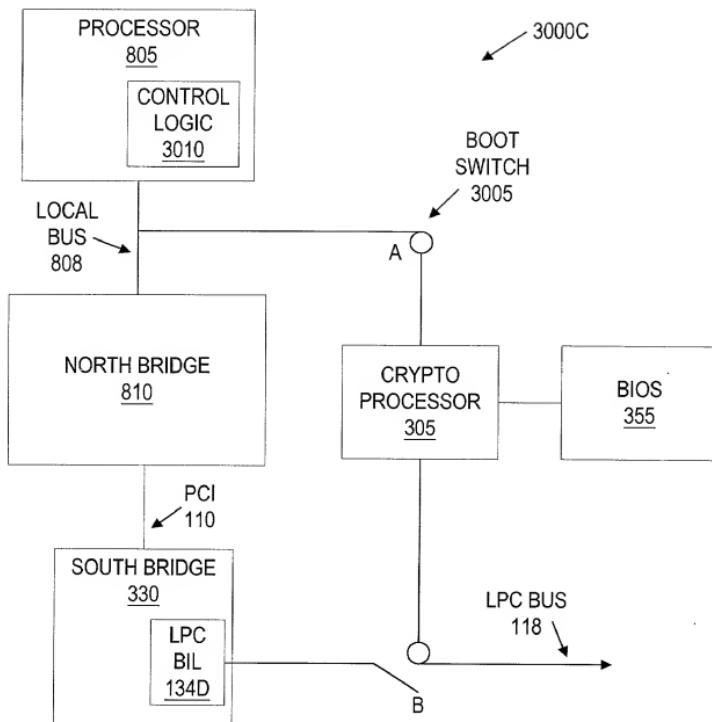
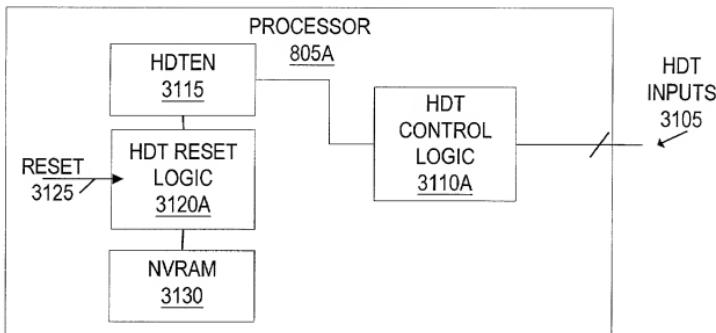
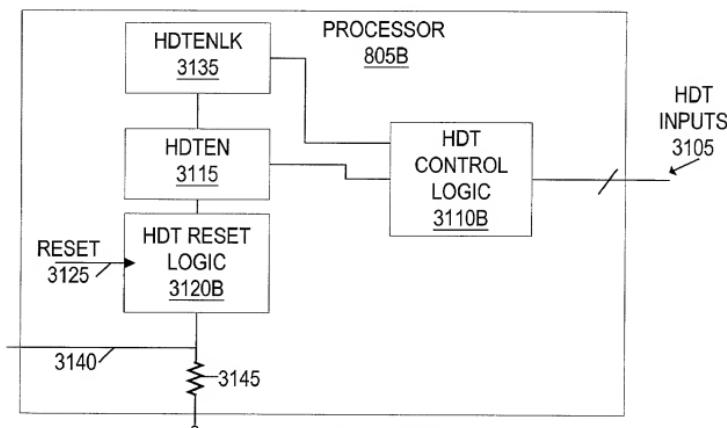


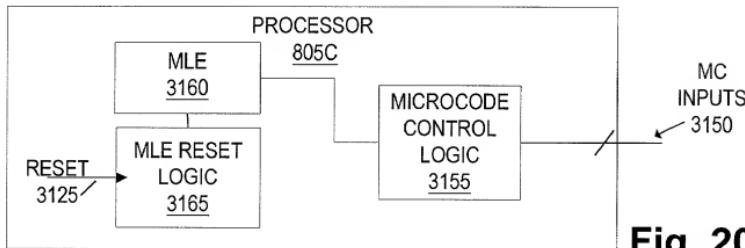
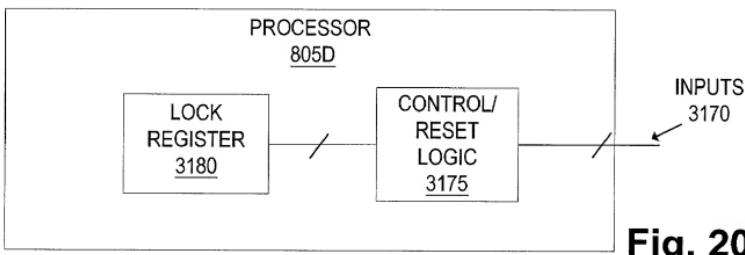
Fig. 19C

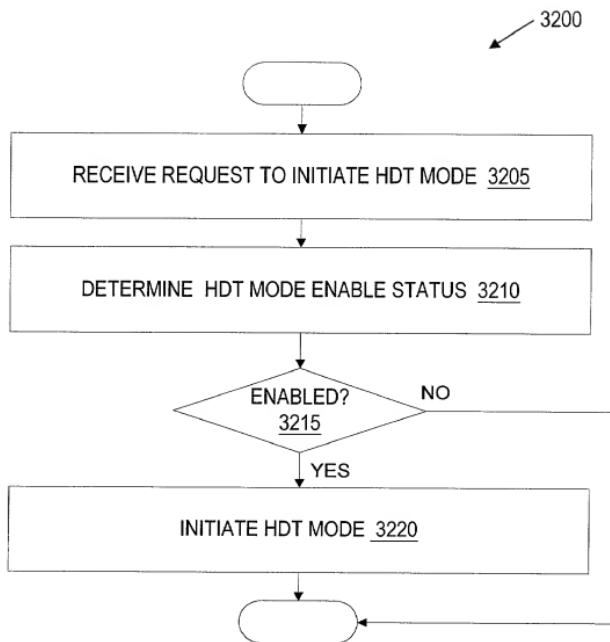


**Fig. 20A**



**Fig. 20B**

**Fig. 20C****Fig. 20D**



**Fig. 21**

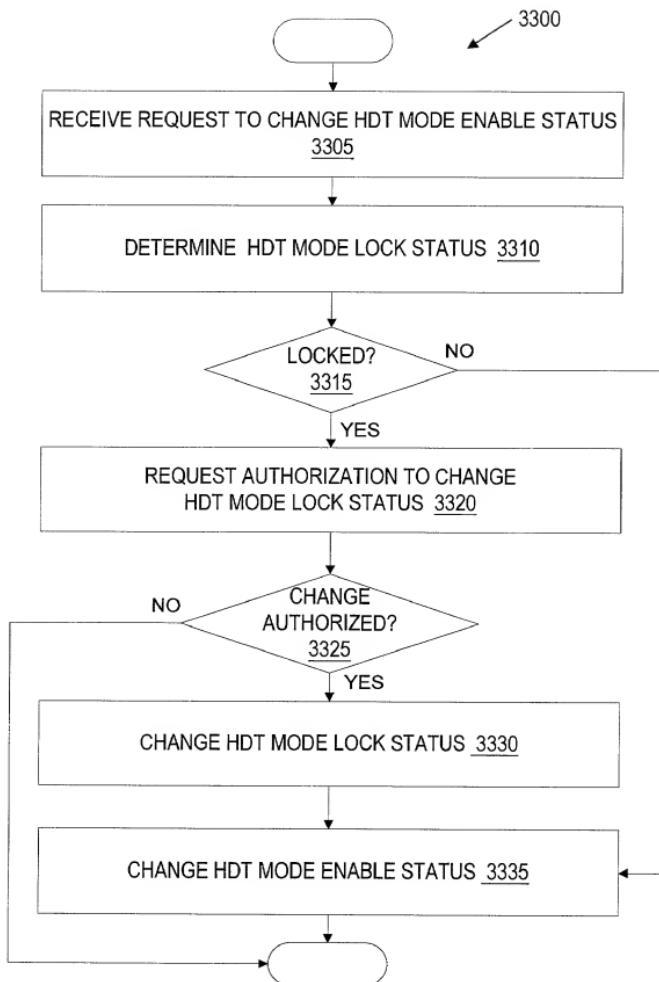


Fig. 22

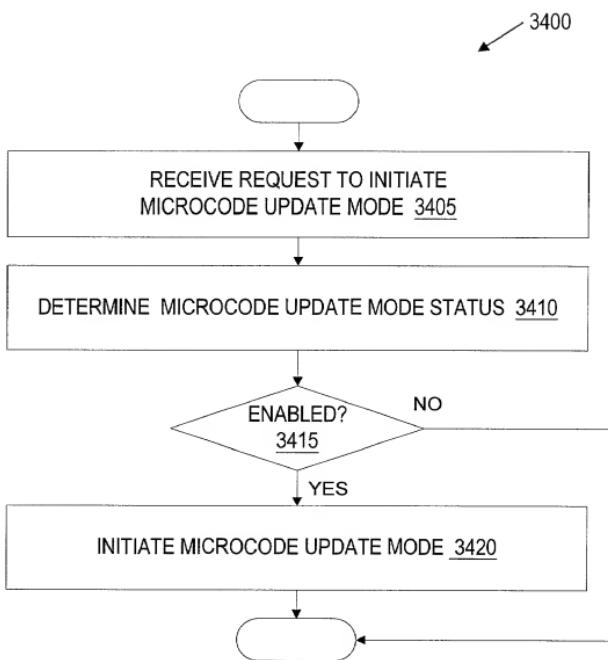


Fig. 23

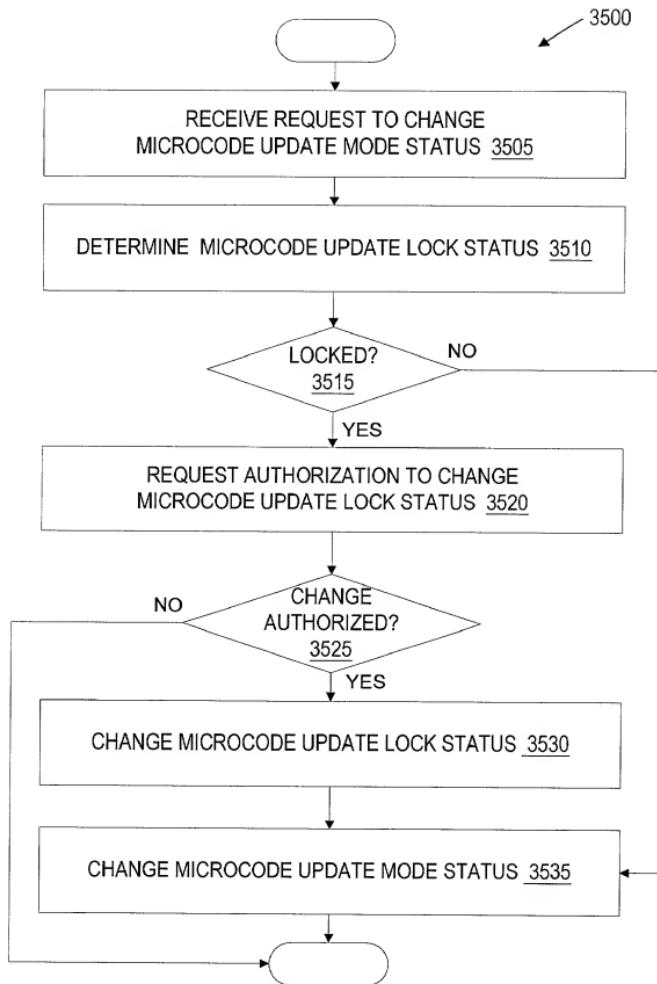
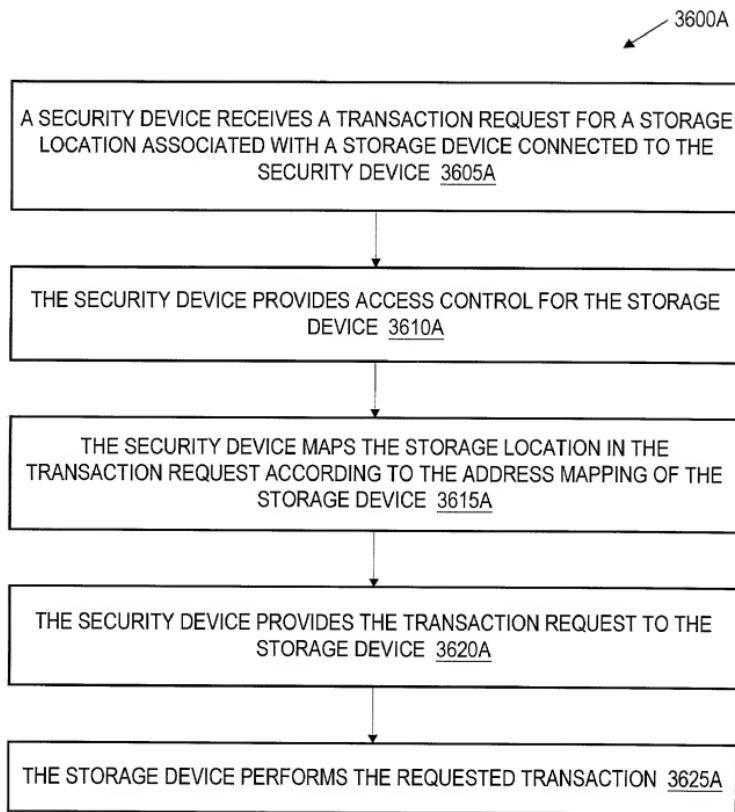


Fig. 24

03853225 - 054101



3600A

Fig. 25A

TTT50-05101-92235860

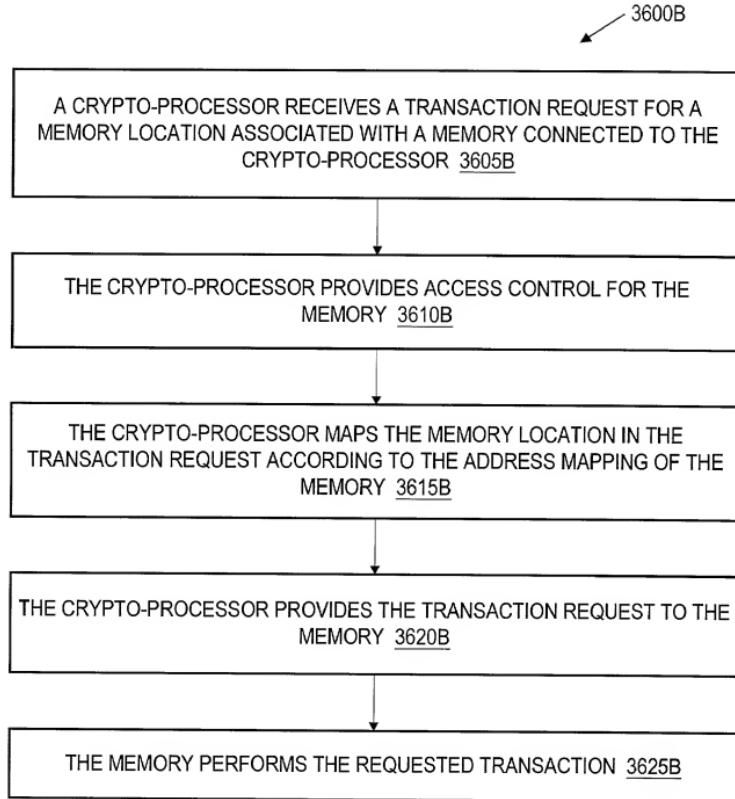


Fig. 25B

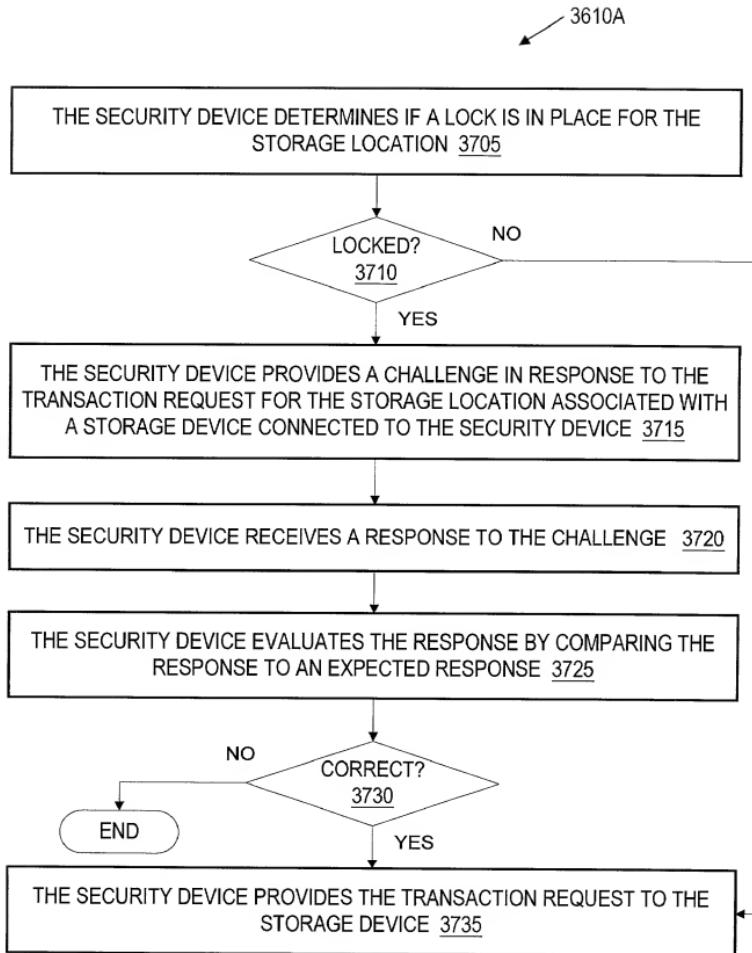
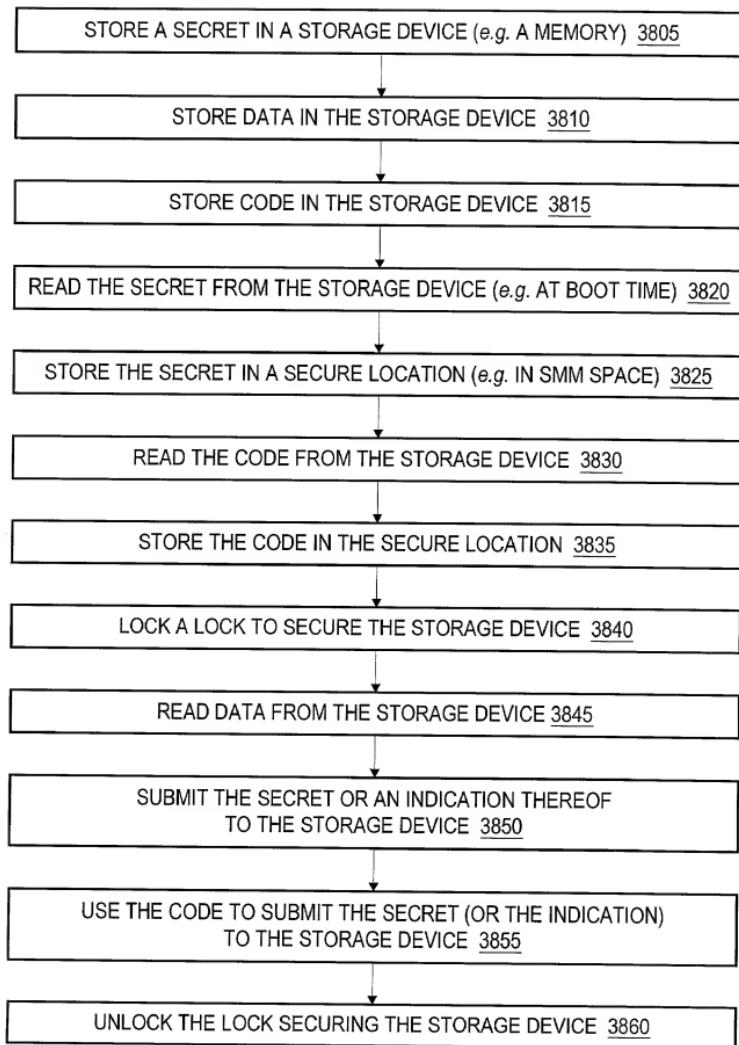
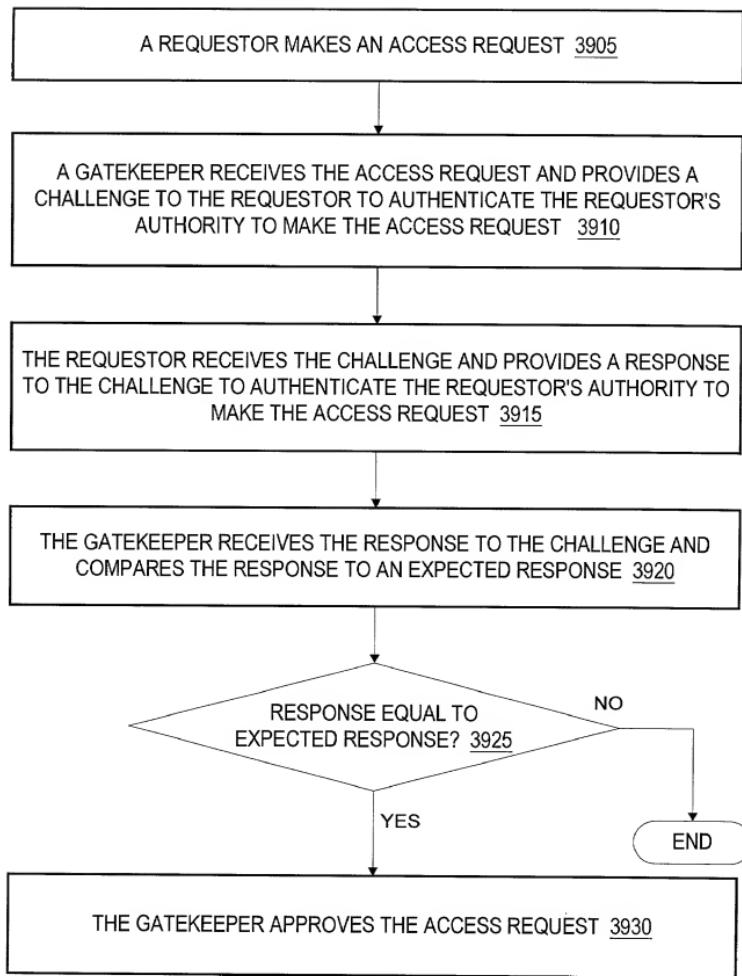


Fig. 26



TTT50-ST2E5860

Fig. 27



**Fig. 28**  
**(Prior Art)**

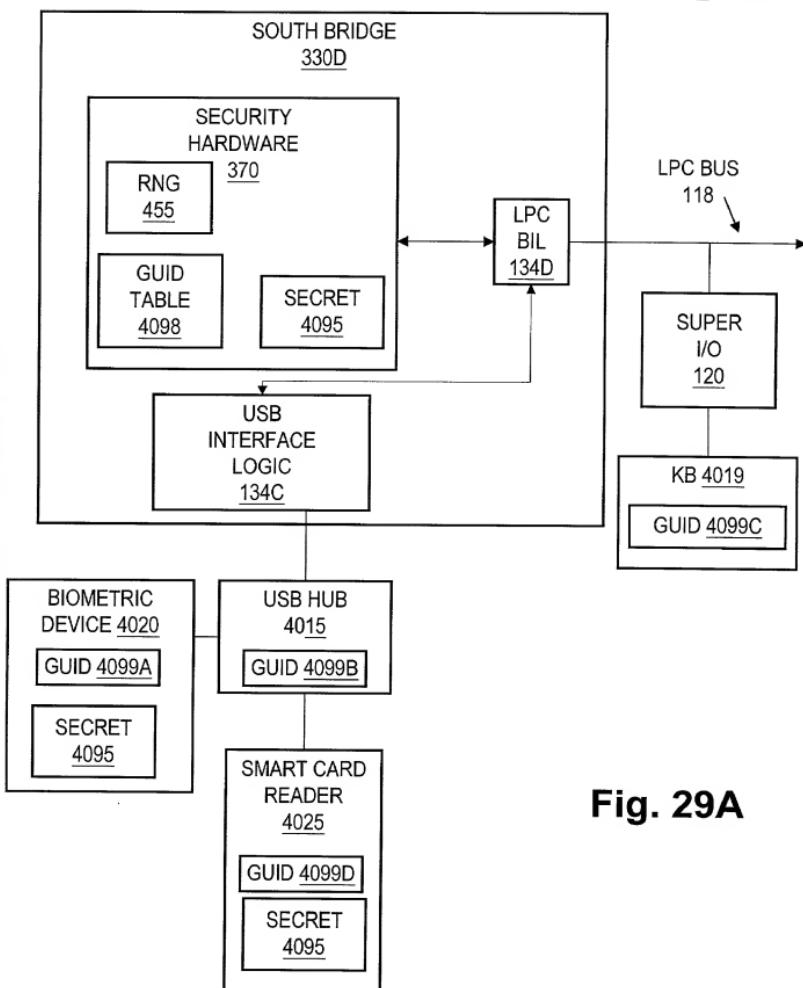


Fig. 29A

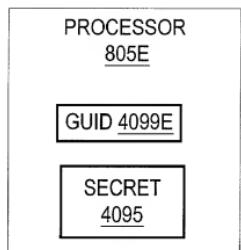


Fig. 29B

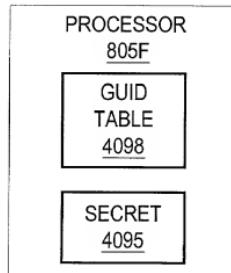


Fig. 29C

09853225 - 051101

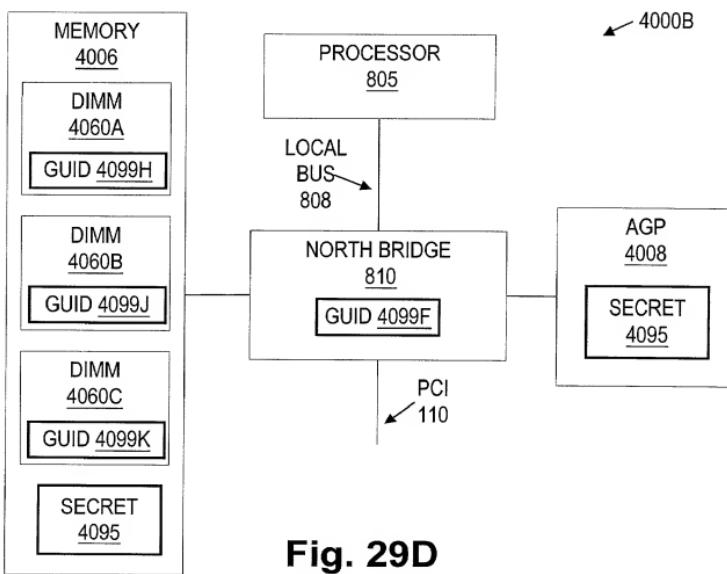


Fig. 29D

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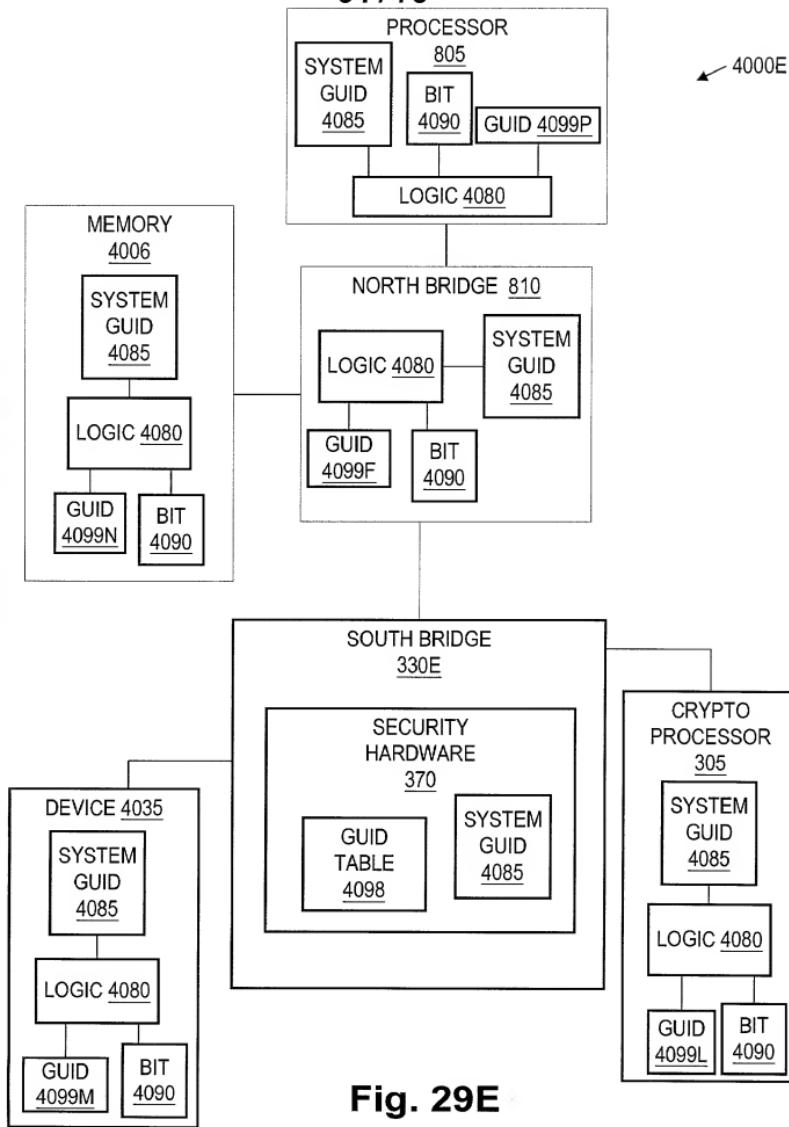
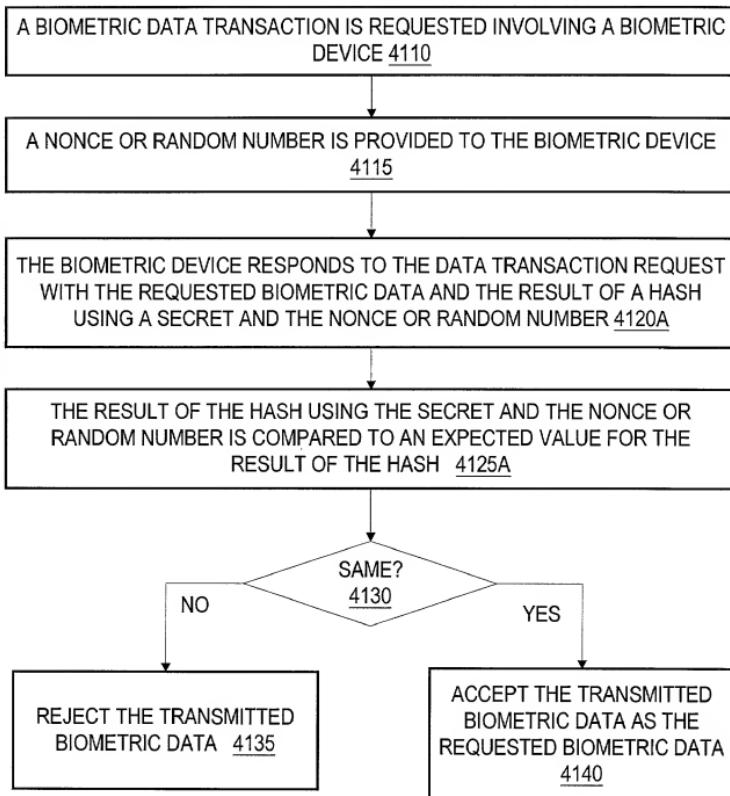


Fig. 29E

0005054101 52253860



4100A

Fig. 30A

4100B

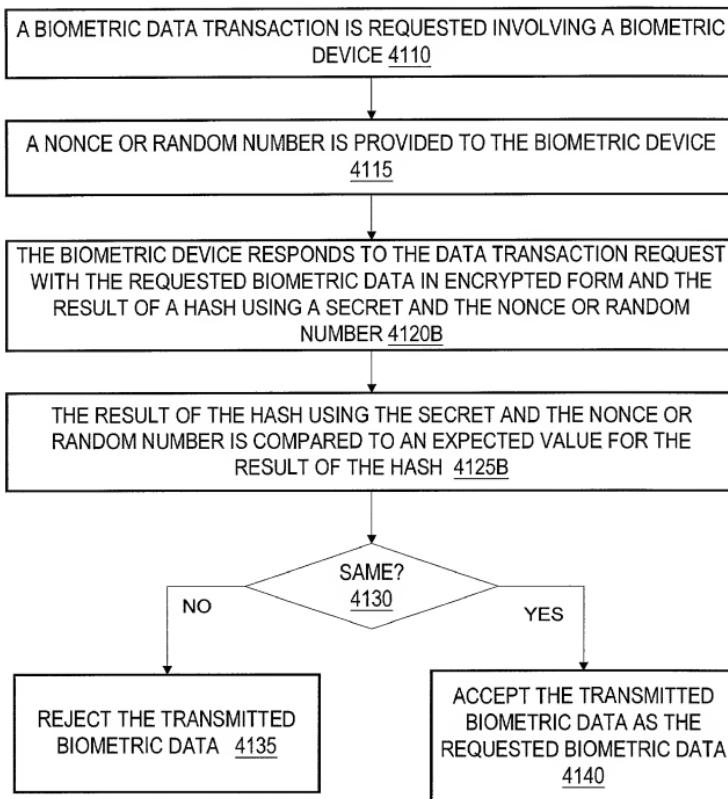


Fig. 30B

010514050225560

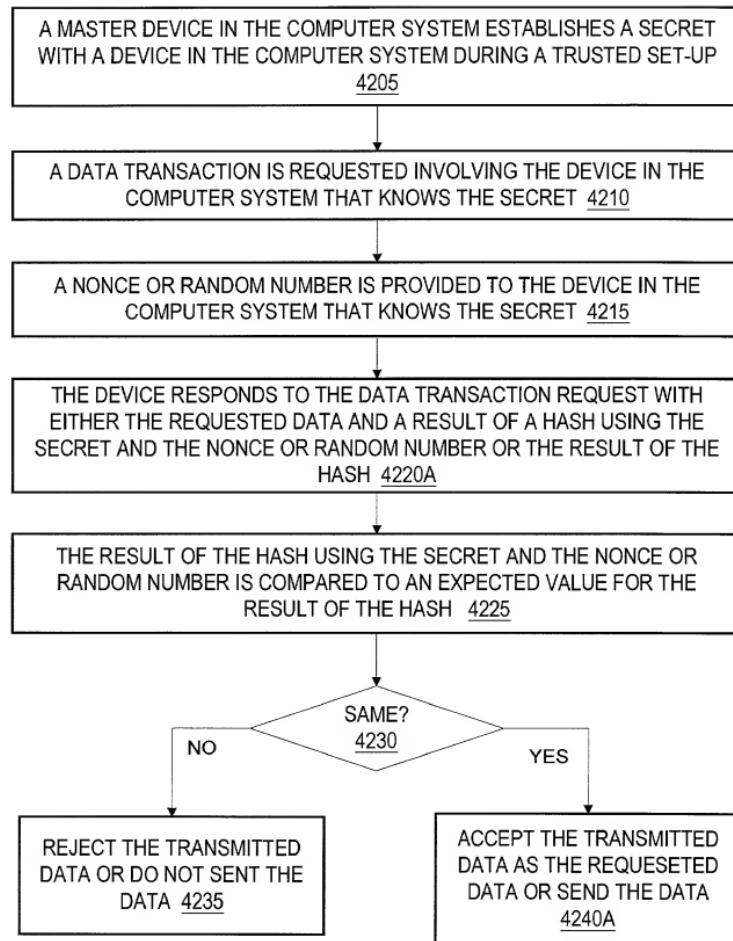


Fig. 31A

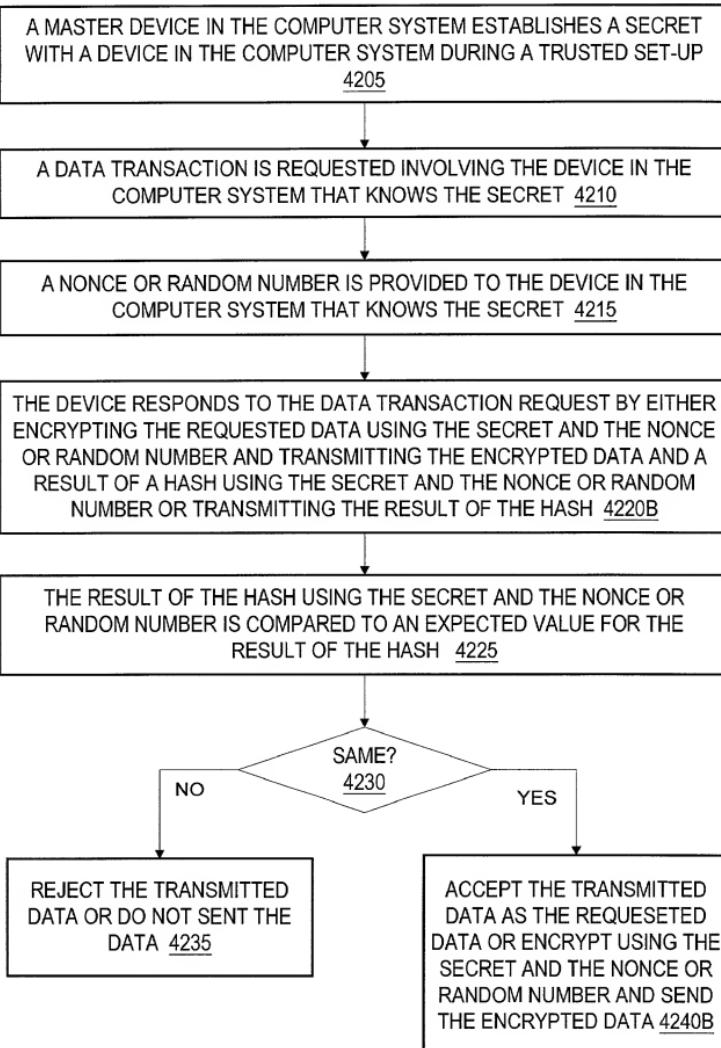


Fig. 31B

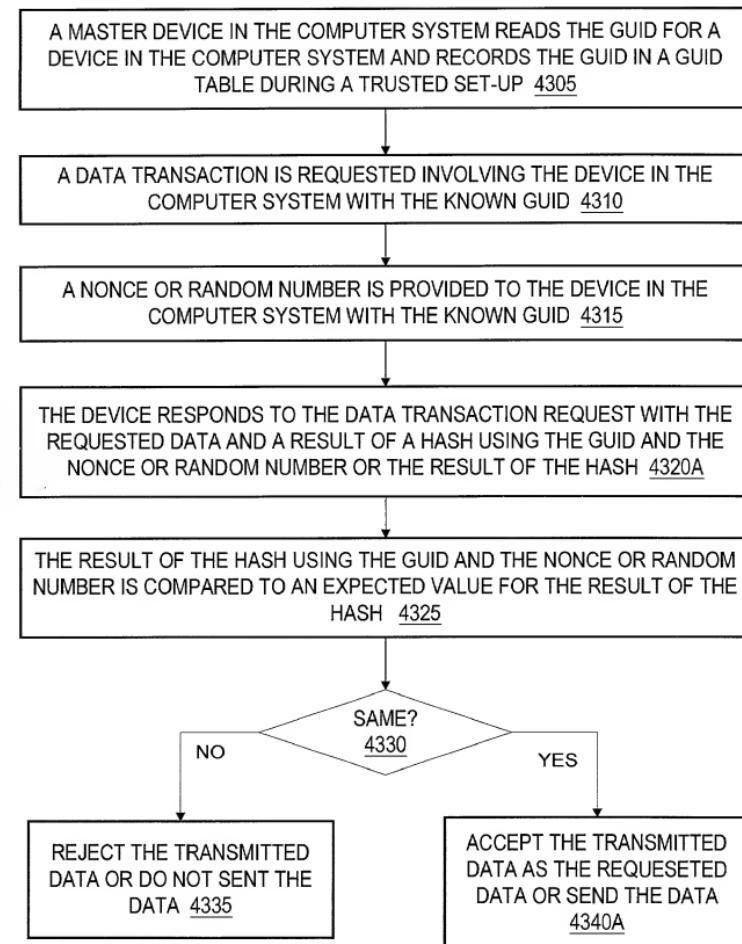


Fig. 32A

4300B

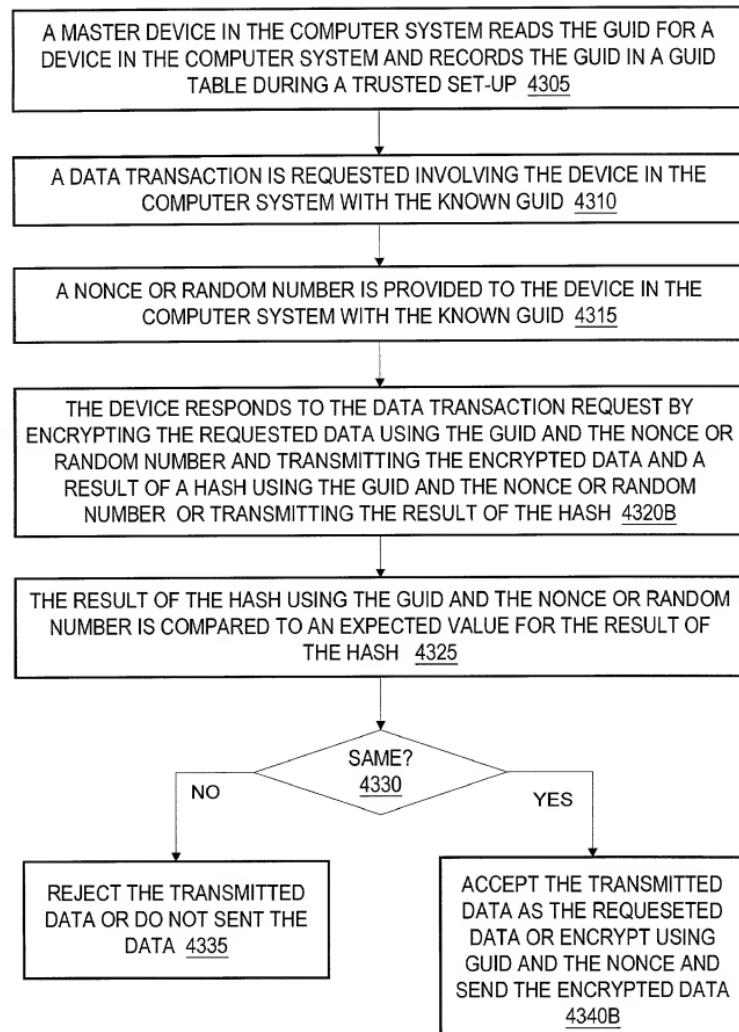


Fig. 32B

A MASTER DEVICE IN THE COMPUTER SYSTEM READS THE GUID FOR A DEVICE IN THE COMPUTER SYSTEM, RECORDS THE GUID IN A GUID TABLE, AND TRANSMITS A SECRET TO THE DEVICE DURING A TRUSTED SET-UP

4306

A DATA TRANSACTION IS REQUESTED INVOLVING THE DEVICE IN THE COMPUTER SYSTEM WITH THE KNOWN GUID THAT KNOWS THE SECRET

4311

A NONCE OR RANDOM NUMBER IS PROVIDED TO THE DEVICE IN THE COMPUTER SYSTEM WITH THE KNOWN GUID THAT KNOWS THE SECRET

4316

THE DEVICE RESPONDS TO THE DATA TRANSACTION REQUEST BY ENCRYPTING THE REQUESTED DATA USING THE SECRET, THE GUID, AND THE NONCE OR RANDOM NUMBER AND TRANSMITTING THE ENCRYPTED DATA AND A RESULT OF A HASH USING THE SECRET, THE GUID, AND THE NONCE OR RANDOM NUMBER OR TRANSMITTING THE RESULT OF THE HASH

4320C

THE RESULT OF THE HASH USING THE SECRET, THE GUID, AND THE NONCE OR RANDOM NUMBER IS COMPARED TO AN EXPECTED VALUE FOR THE RESULT OF THE HASH

4326

SAME?

4330

REJECT THE TRANSMITTED DATA OR DO NOT SENT THE DATA

4335

YES

ACCEPT THE TRANSMITTED DATA AS THE REQUESTED DATA OR ENCRYPT USING THE SECRET, THE GUID, AND THE NONCE AND SEND THE ENCRYPTED DATA

4340C

Fig. 32C

09853285 • 054104

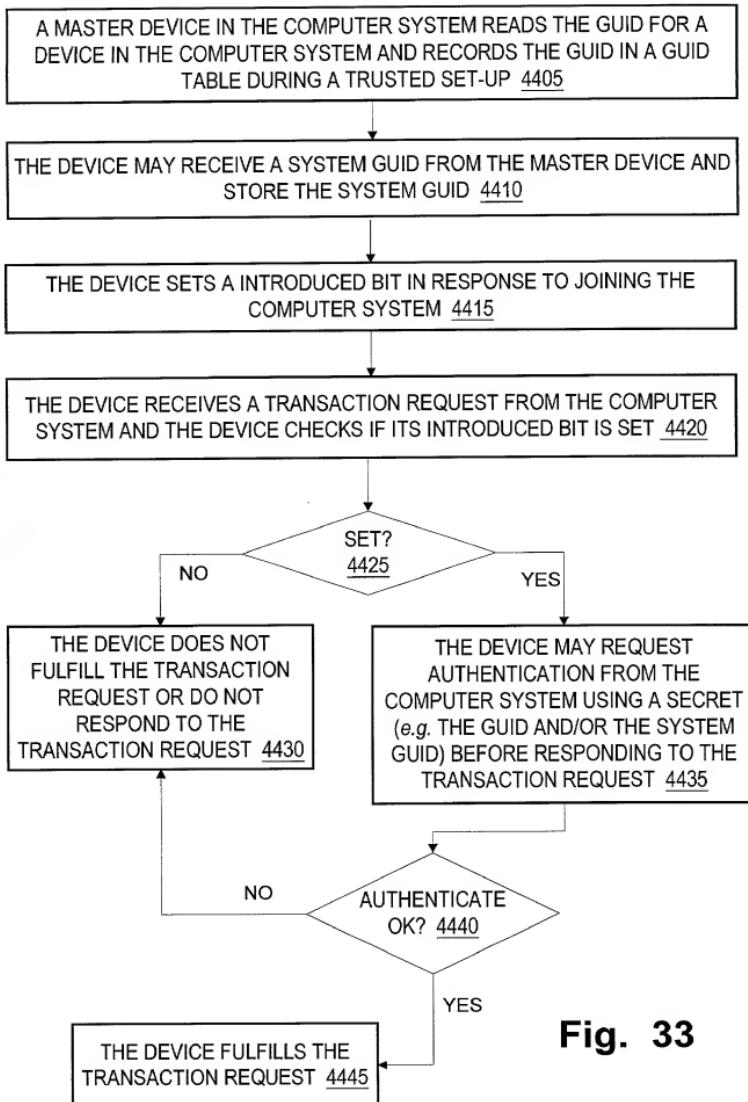
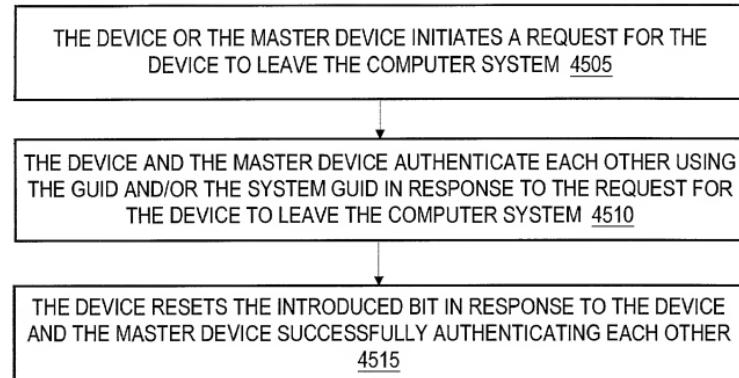


Fig. 33

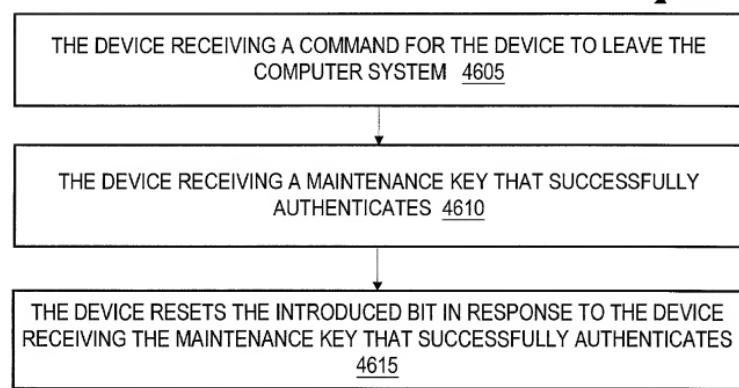
4500



**Fig. 34**

09853225, 051101

4600



**Fig. 35**

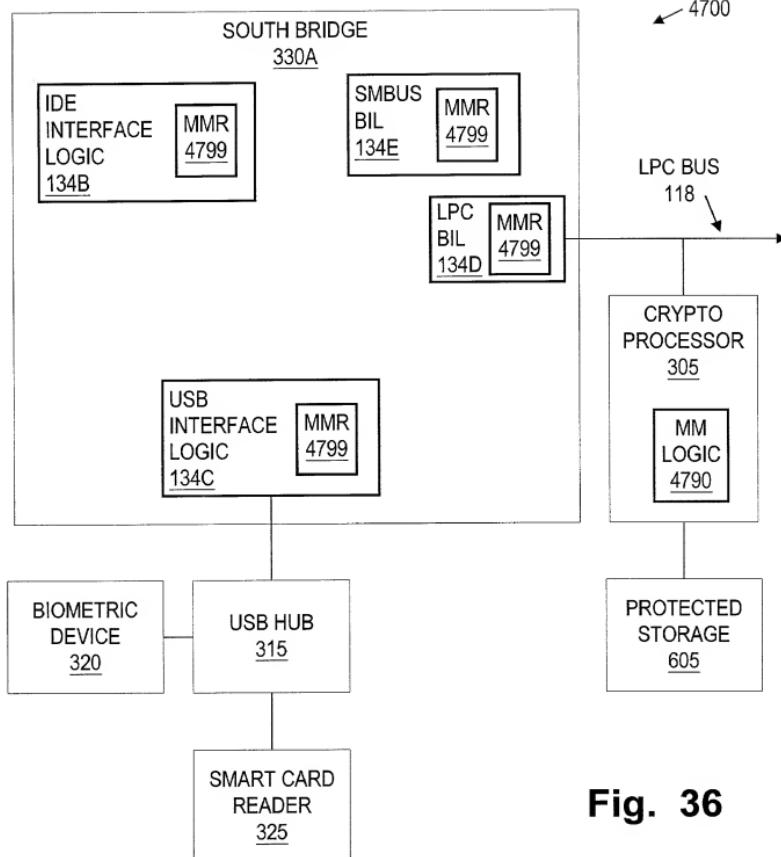


Fig. 36

4800

TRANSMIT A MASTER MODE SIGNAL TO BUS INTERFACE LOGIC CONNECTED BETWEEN MASTER MODE LOGIC AND A DATA INPUT DEVICE, WHERE THE BUS INTERFACE LOGIC INCLUDES A MASTER MODE REGISTER 4805

SET A MASTER MODE BIT IN THE MASTER MODE REGISTER(S) TO ESTABLISH SECURE TRANSMISSION CHANNEL BETWEEN THE MASTER MODE LOGIC AND THE DATA INPUT DEVICE OUTSIDE THE OPERATING SYSTEM OF THE COMPUTER SYSTEM 4810

THE MASTER MODE LOGIC AND THE DATA INPUT DEVICE EXCHANGE DATA OUTSIDE THE OPERATING SYSTEM OF THE COMPUTER SYSTEM THROUGH THE BUS INTERFACE LOGIC(S) THAT INCLUDE THE MASTER MODE REGISTER 4815

THE MASTER MODE LOGIC FLUSHES THE BUFFERS OF THE BUS INTERFACE LOGIC(S) THAT INCLUDE THE MASTER MODE REGISTER AFTER CONCLUDING THE DATA TRANSMISSIONS 4820

THE MASTER MODE LOGIC SIGNALS THE BUS INTERFACE LOGIC(S) TO UNSET THE Maser MODE BITS AFTER FLUSHING THE BUFFERS OF THE BUS INTERFACE LOGIC(S) THAT INCLUDE THE MASTER MODE REGISTER 4825

Fig. 37

09853225 - 051101

09853225 - 051404

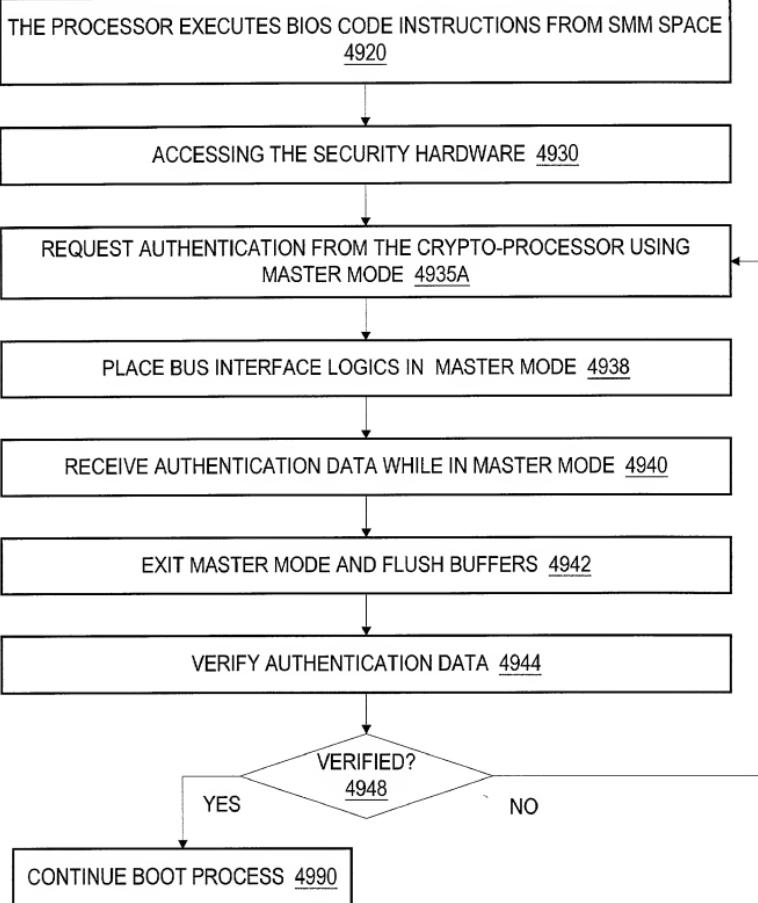


Fig. 38A

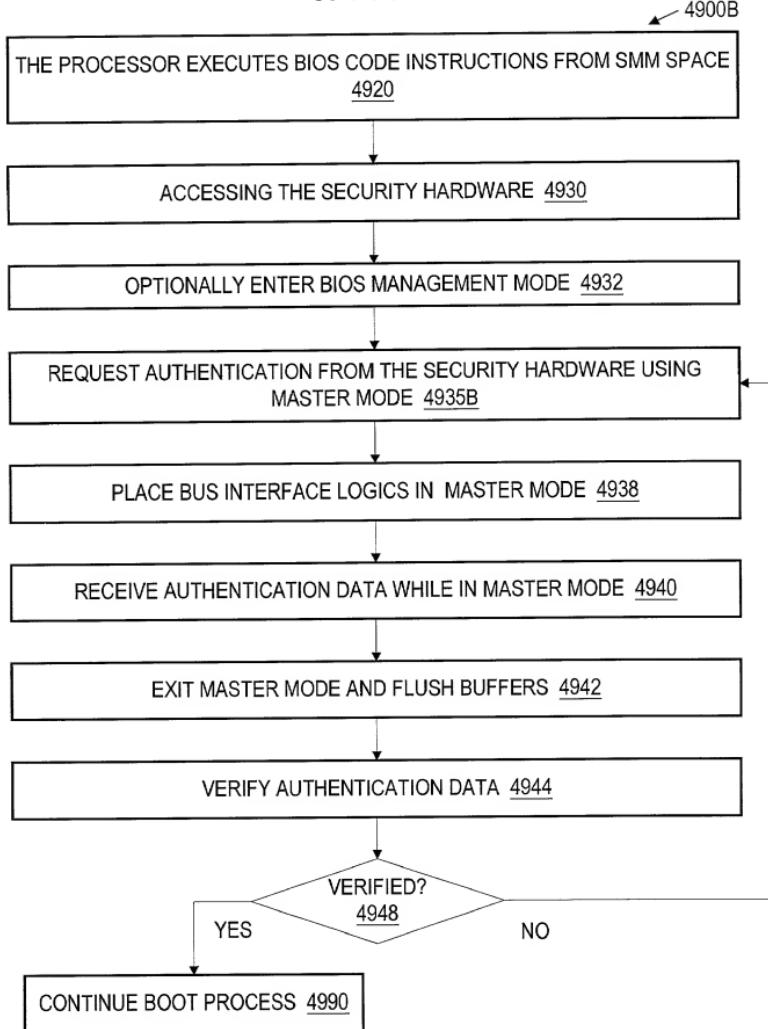


Fig. 38B

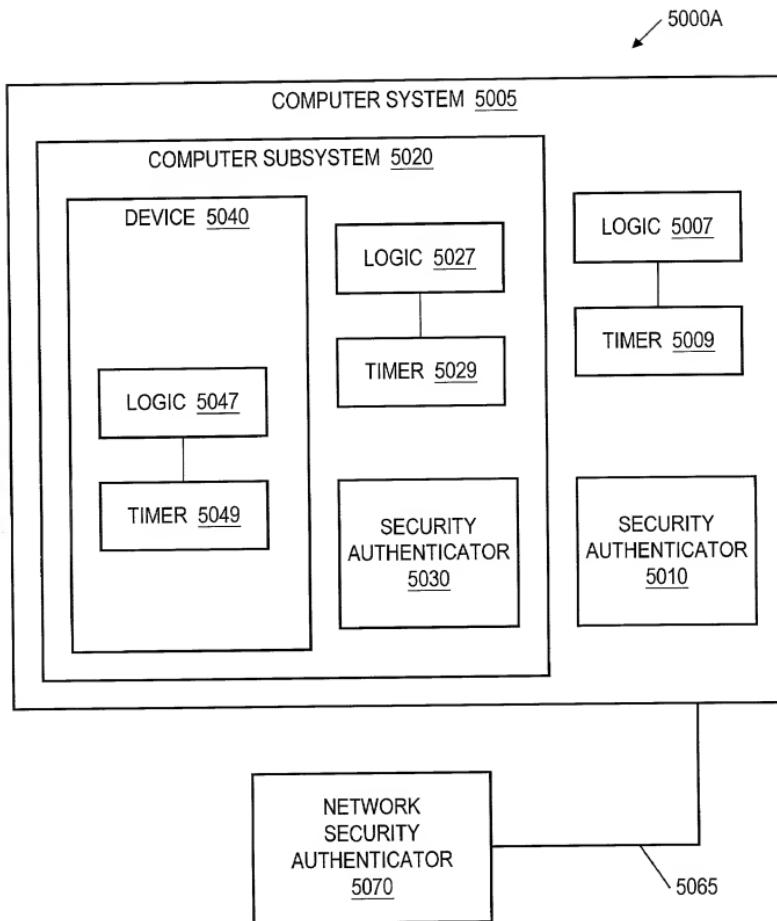


Fig. 39A

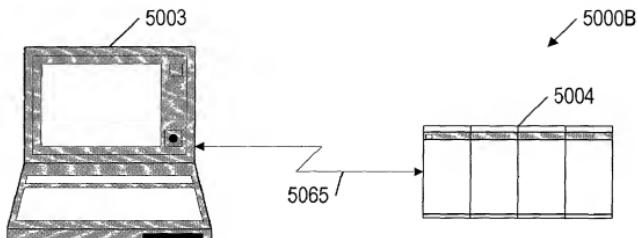


Fig. 39B

100750-52255360

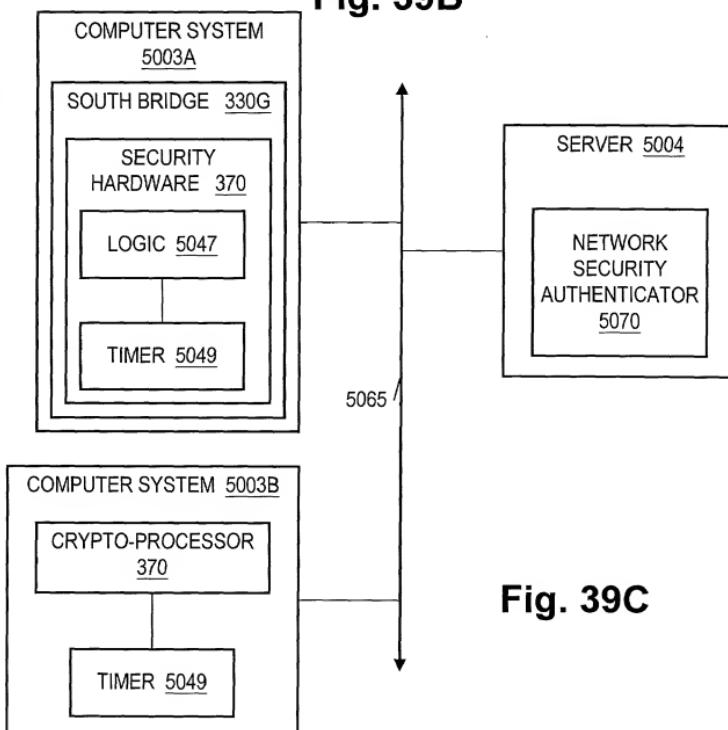


Fig. 39C

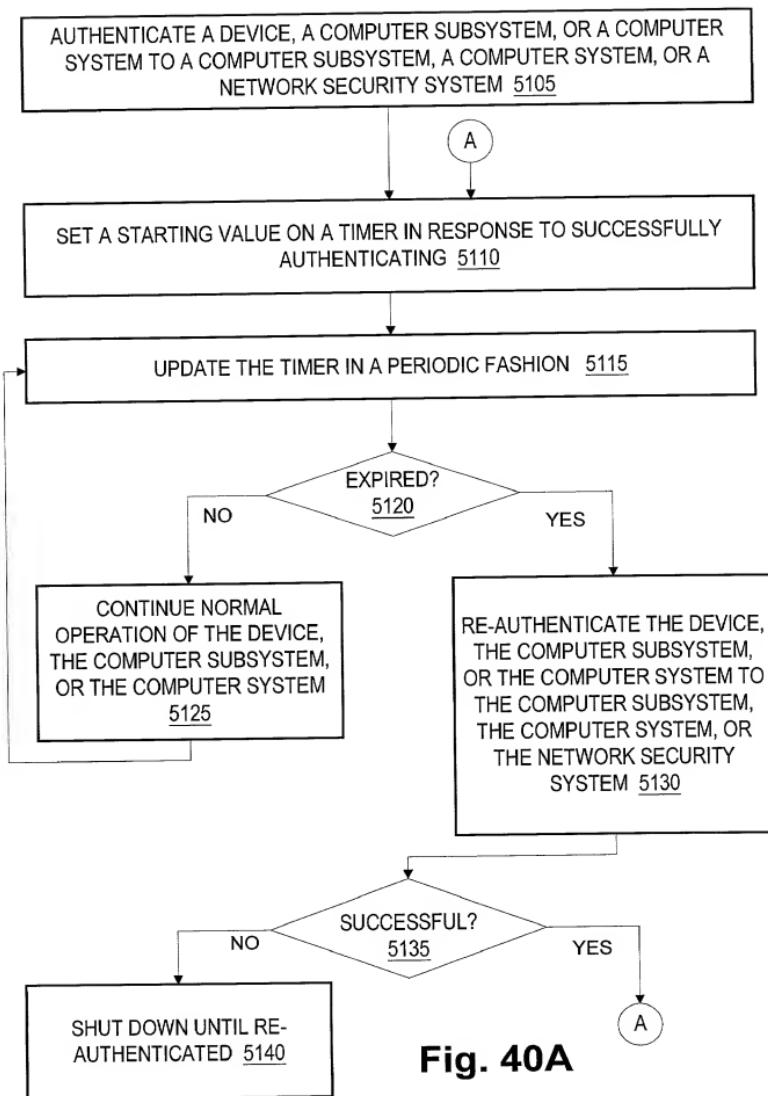


Fig. 40A

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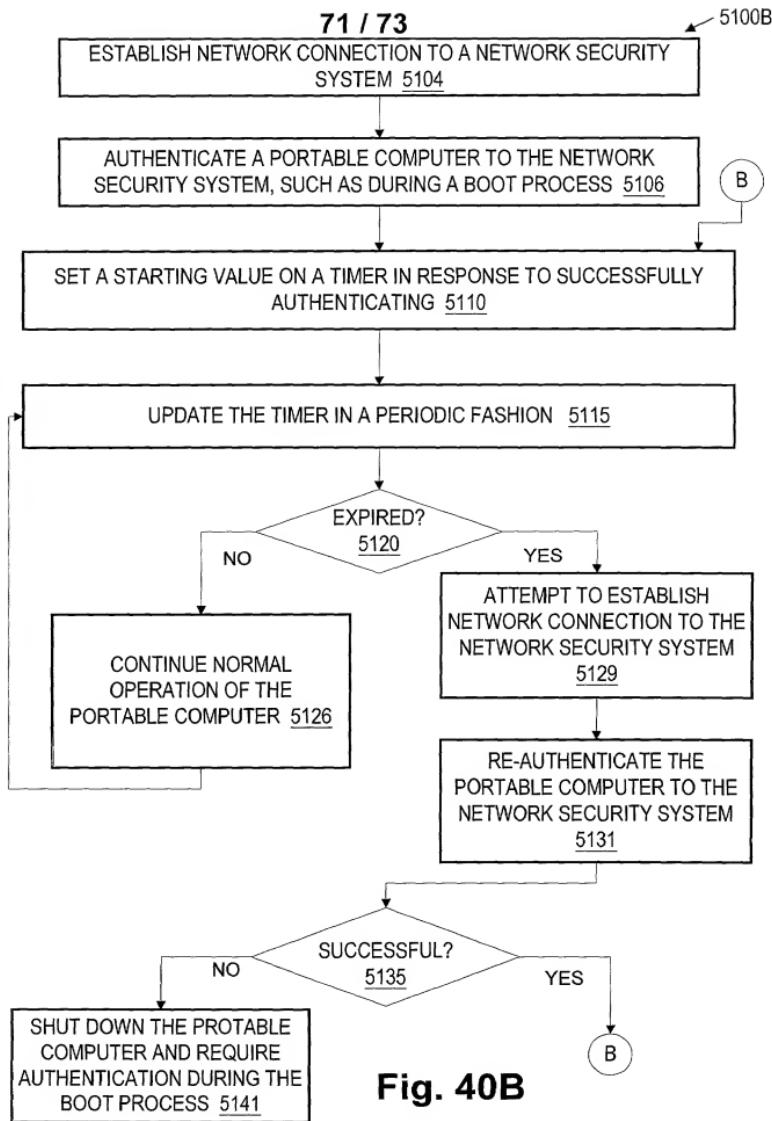


Fig. 40B

03853225 - 051101

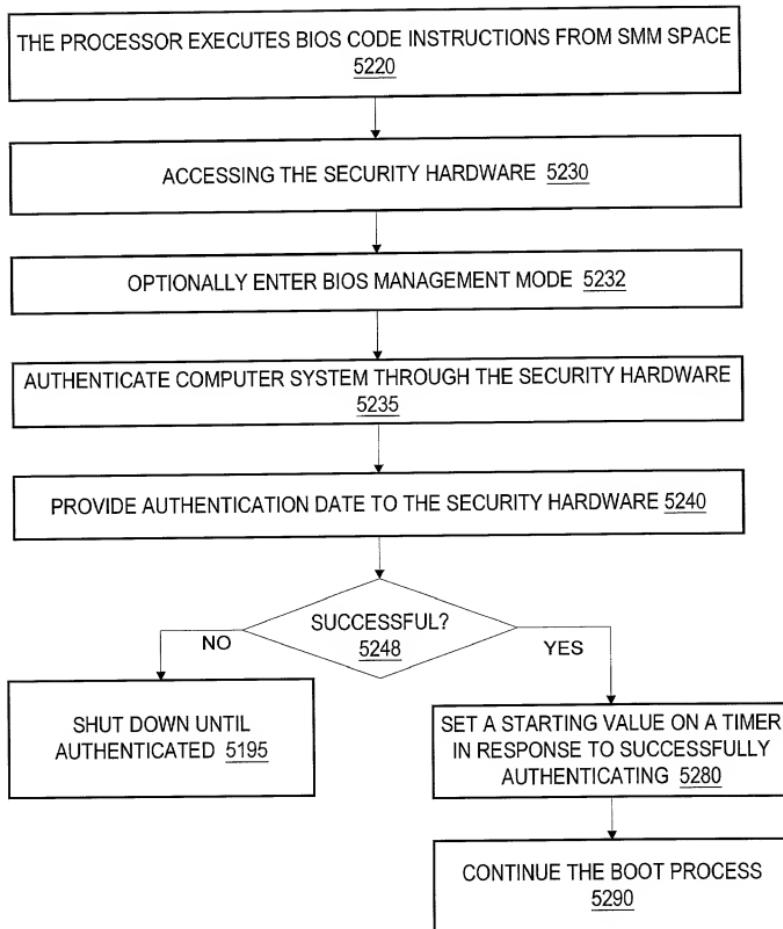


Fig. 41

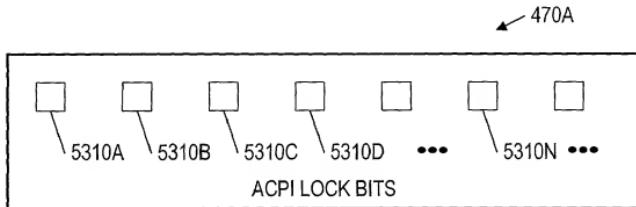


Fig. 42A

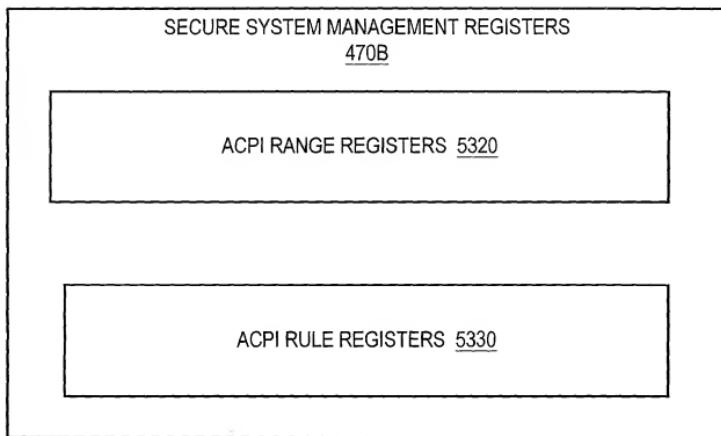


Fig. 42B